

Politecnico di Milano

# Radiation Detection Systems

Hand Notes on the course programme

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a.a. 2023-2024



**POLITECNICO**  
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**Disclaimer:** these are my hand notes on the course of Radiation Detection Systems, taken during the academic year 2023-2024. I made these notes in preparation for the exam, reviewing the theory using mainly the slides. As they are not based on live class notes, some observations made by the professor may be missing.

These notes are not a substitute of the slides. Especially on the last part, some slides content may be missing. I made these notes for myself and did not plan to publish them in advance, so I also apologize for my bad writing and page layout.

Last thing, don't take everything in these notes as true! Some uncertainties are still in it, and maybe even errors I'm not aware of! So please, **question everything** and check yourself if what is written makes sense. I personally suggest in following the course as the professor for me was really clear and also available to discuss every doubt.

Good luck!

# MEGASHEMA RADIATION

Monday, April 29, 2024 10:42 AM

## RADIATION SOURCES

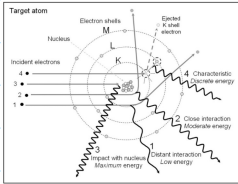
$$E(\text{eV}) = \frac{1240}{\lambda(\text{nm})}$$

$$E = h\nu = \frac{hc}{\lambda}$$

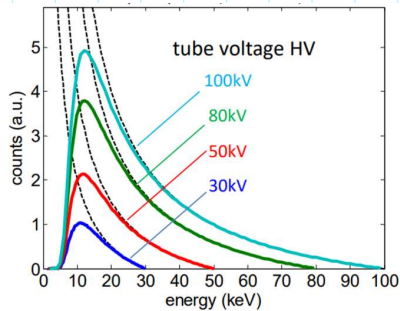
↑  
frequency

X-rays: some eV's

### ENERGY



Hitting material with electrons  
 => curving electrons => accelerating electrons  
 => release of photons



spectrum of electrons including characteristic peaks (XRF)

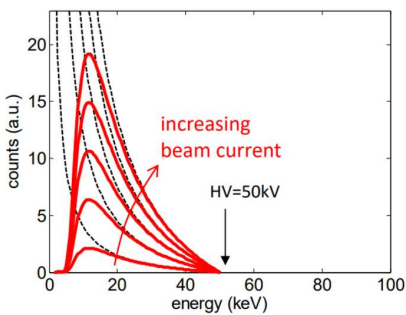
$\frac{ph}{h}$

$$I(h) = \frac{K}{h^2} \left( \frac{h}{h_{\min}} - 1 \right)$$

$$\left[ \frac{ph}{h} \right] \text{ radiation intensity} = \frac{dh}{dh}$$

$$h_{\min} \Rightarrow E_{\max} = Vq \quad V \cdot q$$

$E_{\max}$  proportional to high voltage giving energy to electron



$\frac{ph}{E}$

$$I(E) = \frac{dh}{dh} \cdot \frac{dh}{dE} = \frac{K}{hc} \left( \frac{E_{\max}}{E} - 1 \right)$$

$h = \frac{hc}{E}$

$$E = \frac{hc}{\lambda}$$

$$\left| \frac{dh}{dE} \right| = hc \frac{1}{E^2}$$

$\frac{E_{ph}}{E}$

$$E(E) \cdot I(E) \cdot E = \frac{K}{h} \left( \frac{E_{\max}}{E} - 1 \right)$$

↑  
emitted energy  
( $E_{\text{phoot}}/E(\text{eV})$ )  
↑  
( $\frac{ph}{h} \cdot E_{ph}$ )

$$= \frac{K}{hc} \cdot (E_{\max} - E)$$

$$= \bar{K} (E_{\max} - E)$$

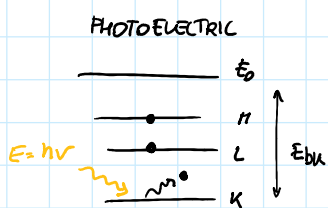
### BREHMSTRAHLUNG X-RAYS

Other radioactive sources ..

- x Synchronous radiation  
 ↳ circular movement of electrons
- x Radioactive demant decay

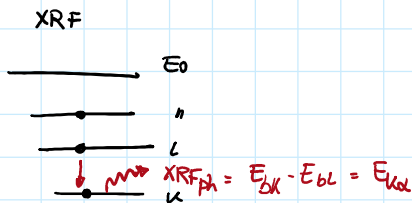
### RADIATION SOURCES

PHOTONS INTERACTION



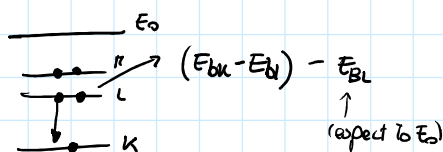
$e_{out} = E - E_{Bk}$

inner shells atom

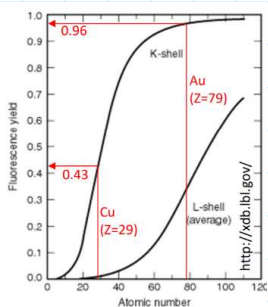


a photon is released with the excess energy gained from the decay  $L \rightarrow K$   
 $\Rightarrow$  photon energy characteristic of material

AUGER



another electron is released with excess kinetic energy from the decay of the second electron

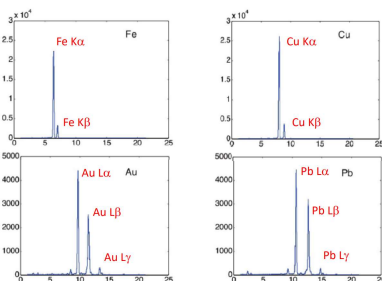
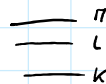


Characteristic X-rays derive from XRF

$\Rightarrow$  fluorescence yield  
 probability of XRF against Auger

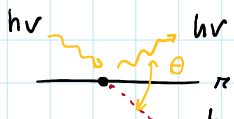
$\Rightarrow$  heavier elements tends to XRF more  
 (Auger is harder in heavier elements, because needs another extraction from an inner shell)

XRF nomenclature:  $K_{\alpha}$ : drop from L ( $\alpha = 1$  above) to K;  $K_{\beta}$ : drop from M ( $\beta = 2$  above) to K;  $L_{\alpha}$ : drop from M ( $\alpha = 1$  above) to L.



possible to do spectroscopy

PHOTOELECTRIC EFFECT



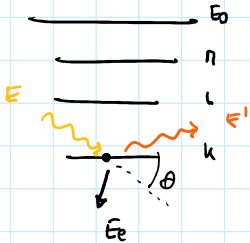
- elastic scattering  $\Rightarrow$  no energy loss
- electron vibrates and

$\lambda = \frac{h}{m \cdot v}$   
 $\uparrow$   
 momentum transfer



- => no energy loss
  - electron vibrates and re-emits the photon
  - change of momentum as direction changes
- momentum transfer

### RAYLEIGH SCATTERING



- inelastic scattering => photon loses energy
- relevant only at high energies

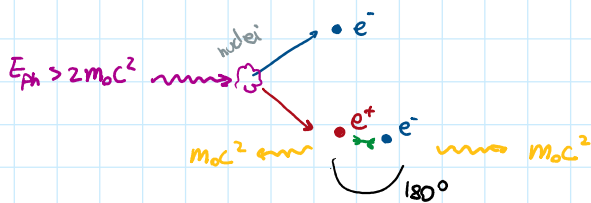
$$E' = \frac{E}{1 + \frac{E}{mc^2}(1 - \cos\theta)}$$

$$E \gg mc^2 \quad 511 \text{ KeV}$$

$$E_e = E - E' - E_k$$

↑ ???

### COMPTON EFFECT



- interaction with nucleus
- extremely HE photon (> 2 rest mass electron)
- e-e+ (positron) pair production
- positron soon annihilates w other electrons => producing 2 photons

### PAIR PRODUCTION

### INTERACTION PROBABILITY

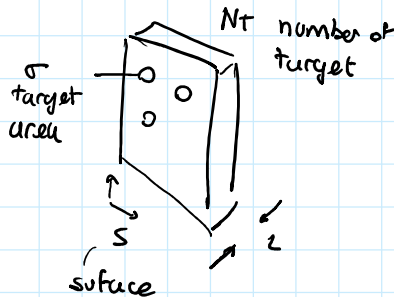
$$P_{int} = \frac{N_t \sigma}{S} = \frac{n_t \cdot \sigma \cdot SL}{S} \quad [1]$$

interaction probability

$$= n_t \sigma L \quad [1]$$

target/cm<sup>3</sup> density

$N_p$  number of particles



$L$  ← attenuation length

$$n_t = \frac{\rho}{Au}$$

$\rho$ : g/cm<sup>3</sup> density  
 $A$ : atomic mass  
 $u$ : atomic mass unit

$$n_t = \frac{1}{A_u} \quad \begin{array}{l} A: \text{atomic mass} \\ u: \text{atomic mass unit} \end{array}$$

$$P_{int} = \frac{\rho}{A_u} \sigma L = \mu \rho L$$

$$\mu = \frac{\sigma}{A_u} \text{ [cm}^2/\text{g]} \quad \text{mass attenuation coefficient}$$

### INTERACTION PROBABILITY

$$\Phi \text{ [} \frac{\text{ph}}{\text{A}\cdot\text{s}} \text{]} = \frac{N_{ph}}{S \cdot \Delta t}$$

↑ incident flux density (and rate)?

$$N_{events} = N_{ph} \cdot P_{int} = \Phi S \Delta t \frac{N_t \sigma}{S}$$

$$= \Phi N_t \sigma \Delta t$$

$$R_{events} = \frac{N_{events}}{\Delta t} = \Phi N_t \sigma$$

cross section is found and defined experimentally from ↑

$$\sigma = \frac{N_{events}}{\Phi N_t \Delta t} = \frac{N_{events}}{N_{ph}} \cdot \frac{S}{N_t} = \frac{N_{ev}}{N_{ph} n_t L} \quad N_t = n_t \cdot S L$$

### EVENT RATE

For different events, we define different target cross section

⇒ the total cross sections takes all possible interactions into account

$$\sigma = \sigma_{\text{photoelectric}} + \sigma_{\text{incoh}} + \sigma_{\text{coh}} + \dots$$

↑ incoherent (inelastic) scattering      ↑ elastic (coherent) scattering

derives

$$\Rightarrow \mu = \mu_{pe} + \mu_{incoh} + \mu_{coh} \dots$$

For mixed materials / compounds

$$\mu = \sum_i w_i \mu_i \text{ [cm}^2/\text{g]}$$

↑ relative mass weight in the compound

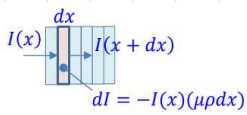
$$\begin{aligned} S &= N_t \sigma = n_t S L \sigma \\ S &= N_{t1} \sigma_1 + N_{t2} \sigma_2 \\ &= (N_{t1} \sigma_1 + N_{t2} \sigma_2) S L \\ &= \left( \frac{\rho_1}{A_{u1}} \sigma_1 + \frac{\rho_2}{A_{u2}} \sigma_2 \right) S L \\ &= (\mu_1 \rho_1 + \mu_2 \rho_2) S L \\ &= \frac{\text{cm}^2}{\text{g}} \cdot \frac{\text{g}}{\text{cm}^3} \cdot \text{cm}^2 \end{aligned}$$

### COMPOSITE MASS ATTENUATION COEFFICIENTS

$$P_{int} = \mu \rho L$$

$$\Rightarrow P_{int} = \mu \rho \text{ [cm}^{-1}\text{]} \quad \text{probability per unit length}$$

probability per unit length



$$I(x+dx) - I(x) = -I(x) \mu p dx \quad \frac{dI}{dx} = -\mu p I(x)$$

$$\int_{I_0}^{I(x)} \frac{dI}{I(x)} = \int_0^x -\mu p dx$$

$$\left[ \ln(I) \right]_{I_0}^{I(x)} = -\mu p x$$

$$\ln\left(\frac{I(x)}{I_0}\right) = -\mu p x$$

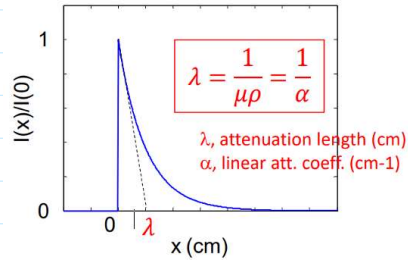
$$I(x) = I_0 e^{-\mu p x}$$

$$I(x) = I_0 e^{-x/\lambda}$$

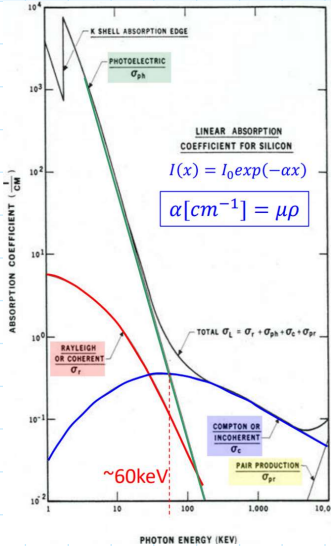
$$h \text{ (cm)} \text{ attenuation length} = \frac{1}{\mu p} = \frac{1}{\alpha}$$

$$\alpha \text{ (cm}^{-1}\text{)} = \mu p = \mu p$$

attenuation coefficient



### ATTENUATION LENGTH



$$\alpha = \frac{1}{h} \text{ absorption coefficient}$$

- dependence on photon energy
- different types of interaction

=> note the photoelectric K-shell jump when becomes

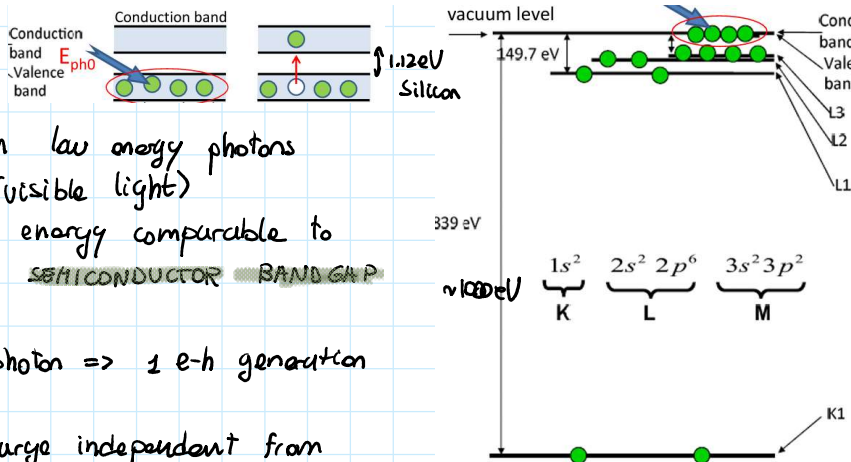
$$E_{ph} = h\nu > E_k$$

### INTERACTION COMPONENTS

### CHARGE GENERATION

Electron devices detect charge, so we must relate the photon interaction to a charge effect

VISIBLE - IR - UV light ~ 1 eV



With low energy photons  
(visible light)  
=> energy comparable to  
SEMICONDUCTOR BAND GAP

1 photon => 1 e-h generation

Charge independent from  
photon energy

### E-H PAIR VISIBILE LIGHT

A x ray photon interacts  
with photoelectric effect  
with K shell ~ 1000 eV

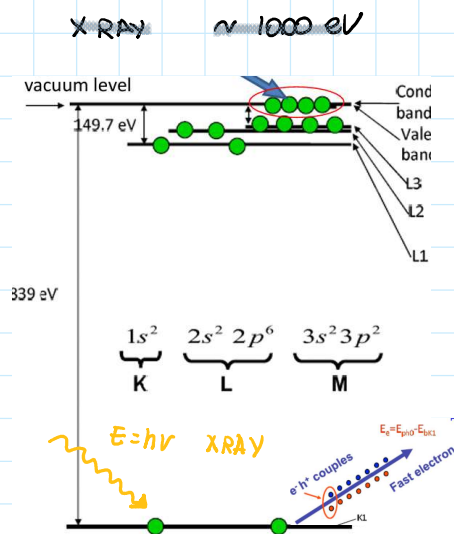
=> The free electron has  
a high kinetic energy

=> The high kinetic energy

$$E_{\text{free}} = h\nu - E_{\text{Kshell}}$$

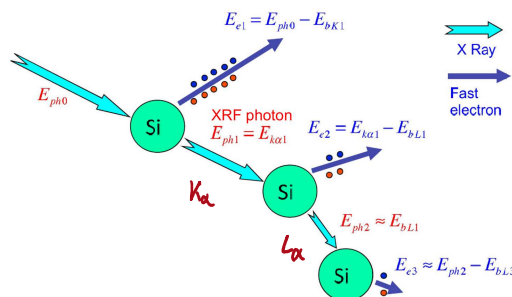
is dissipated through the creation of  
many e-h pairs (~1 eV) in conduction/valence

charge dependant on  
photon energy



### E-H PAIR GENERATION X RAYS

With high energy  
X RAYS a chain  
reaction can occur  
realising more  
XRF photons from  
different atoms in  
the materials



Problem because we will see XRF photons detected, but in reality they are coming from the detector itself

### XRF CHAIN

The relation between radiation energy converted into e-h

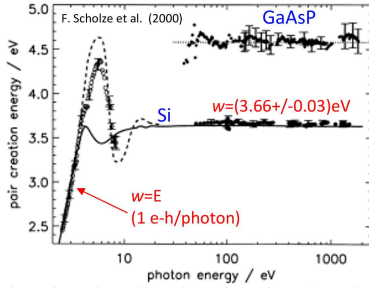


The relation between radiation energy converted into e-h is a property of the material

$$w(E) = E / \langle N \rangle$$

$$w = 3.63 \text{ eV Si (300K)}$$

$$w = 2.96 \text{ eV Ge (300K)}$$



$N$  number of generated e-h pairs

$$\langle N \rangle = \frac{E}{w(E)} = \frac{h\nu}{w(E)}$$

$w$  constant for photoelectric XRAY generation

$w$  constant  
 $w(E) \propto E \Rightarrow \langle N \rangle \propto E$   
 $\langle N \rangle$  constant XRAY  
 $\Rightarrow$  independent on photon  $E$   
 LIGHT - UV - IR

$$\sigma^2(N) = F \cdot \langle N \rangle = F \cdot \frac{E}{w}$$

$F$  FANO FACTOR

$\Rightarrow$  variance of generated e-h pair coefficient

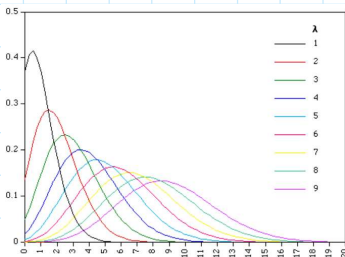
Where

$$F = 0.12 \text{ Si}$$

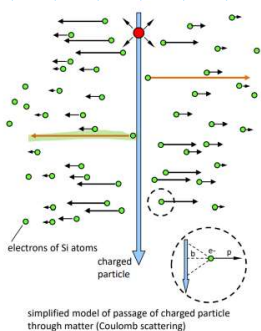
$$F = 0.13 \text{ Si}$$

$N$  follows a poissonian (modified by fano factor, so  $\sigma^2(N) \neq \langle N \rangle$ ) distribution

$\hookrightarrow$  tends to gaussian when  $\langle N \rangle$  big



## CHARGED PARTICLES INTERACTIONS

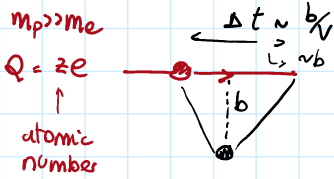


charged particles entering the device lose energy

- by giving kinetic energy to nearby electrons, ionizing He atoms ( $\mu^+ + \text{atom} = \mu^+ + \text{atom}^+ + e^-$ )
- by close collisions that generate gamma rays
- by emissions of a photon due to the deceleration induced by

- by emissions of a photon due to the deceleration induced by the substrate

CHARGE INTERACTIONS



Assumptions

- Energy transfer >> binding energy
- Electron at rest during interaction (impulse transfer)
- ↳ whole momentum transferred to electron

Model:

$$F_{\perp} = \frac{1}{4\pi\epsilon} \frac{ze^2}{b^2} \quad \text{Coulomb force} \sim \frac{ze^2}{b^2}$$

$$\Delta t = \frac{b}{v} \quad \text{interaction time}$$

$$I_{\perp} = m_e v_e = \int F dt = \frac{ze^2}{b} \cdot \frac{1}{v} \quad \text{impulse = momentum transfer}$$

$$\hookrightarrow v_e = \frac{ze^2}{bv m_e}$$

$$W = \frac{1}{2} m_e v_e^2 = \frac{p^2}{2m_e} = \frac{z^2 e^4}{b^2 v^2 2m_e}$$

I struggled on this part, so check!

↳ Energy transfer in a material

$$dE = \frac{z^2 e^4}{bv^2 2m_e} \cdot v dt \Rightarrow \frac{dE}{dx} = \frac{z^2 e^4}{v^2 2m_e b}$$

=> integrate over [bmin, bmax]

We then obtain Bethe-Block formula

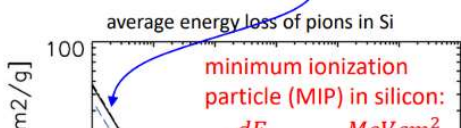
↳ Energy loss is normalized by target density

$\propto \frac{1}{v^2} \propto \frac{1}{E^2} \text{ ??}$

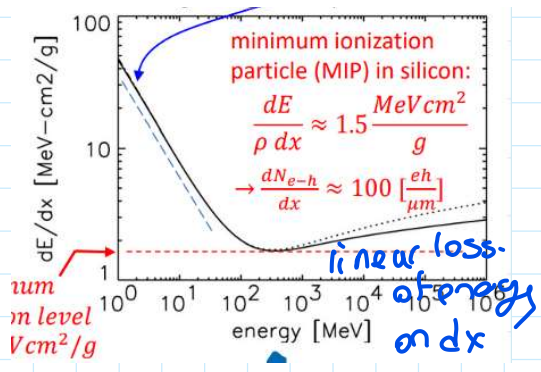
$$\frac{1}{\rho} \frac{dE}{dx} \propto \frac{Z^2}{A\beta^2} \propto \frac{1}{E} \propto \left[ \frac{\text{MeV}}{u} \frac{cu^3}{g} \right] \propto$$

to normalize term with density  $\hookrightarrow \frac{v^2}{c^2} \propto \text{Energy}$  of incoming particle (due to  $1/v^2 \Rightarrow$  velocity of ionizing incoming particle)

$\frac{1}{E^2}$  if plotted against energy



Using mean creation energy ( $W_0$  in silicon)

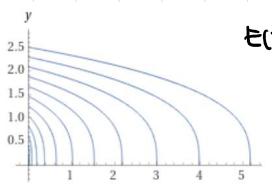


Using mean creation energy ( $W_e$  in silicon)  
 $\Rightarrow$  we can derive number of electron hole pairs generated with the energy released by the incoming of radiation in silicon

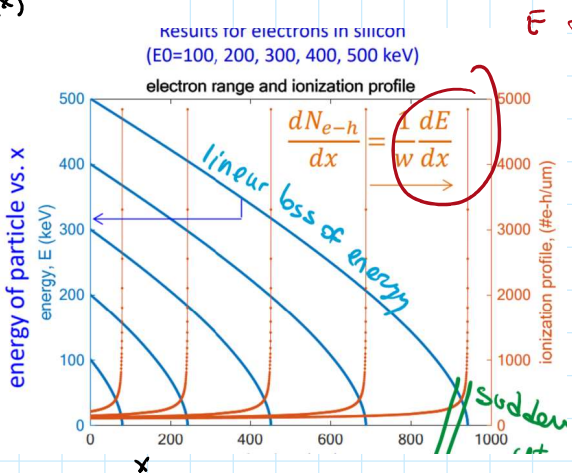
$$\Rightarrow \frac{dN_{e-h}}{dx} = \frac{dN_{e-h}}{W_e dx} \approx 100 \frac{eh}{\mu m}$$

Ionization profile is obtained through integration

$$-\frac{dE}{dx} = f(E) \propto \frac{1}{E^2} \quad \Leftrightarrow \text{differential equation}$$



$E(x)$

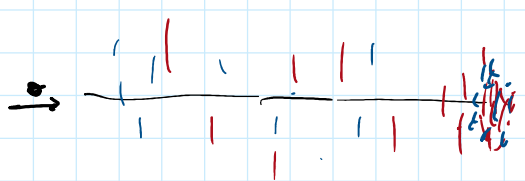


$E$  tends to 0 near the end

$$\frac{dE}{dx} \propto \frac{1}{E^2} \downarrow$$

$$\frac{dE}{dx} \uparrow$$

Big energy released near the end!



trajectory with big drop of energy near the end

# MEGASCHEMA SEMICONDUCTORS

Monday, June 24, 2024 2:39 PM

1) PAIR CREATION ENERGY 2-5 eV (3.63 eV Si)

- low: large number of charges in detector per photon
- higher energy resolution => high S/N ratio

2) ENERGY GAP 1-3 eV

- small leakage current (good medium high bandgap)
- L< less shot noise

3) DENSITY  $\rho = 2-10 \text{ g/cm}^3$

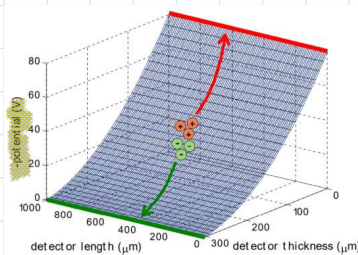
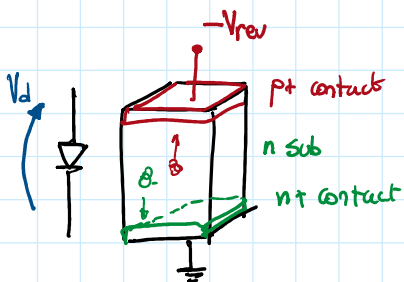
- high energy loss per unit length for ionized part. detection

$$\frac{dN}{dx} = 100 \text{ eh}/\mu\text{m Si MIP}$$

(minimum ionization particle)

- L -> thin detectors
- L -> precise position measurement

## WHY SEMICONDUCTOR

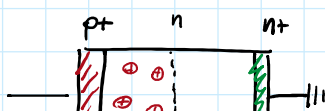


pn junction is reversed

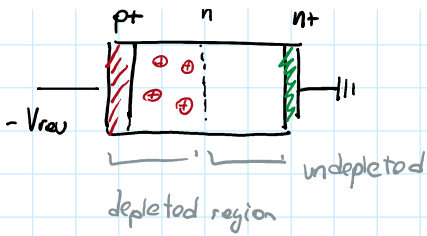
$$C_{dep} = \epsilon_0 \epsilon_r \frac{A}{t_{dep}}$$

$A$  -> detector active area  
 $t_{dep}$  -> depletion region width

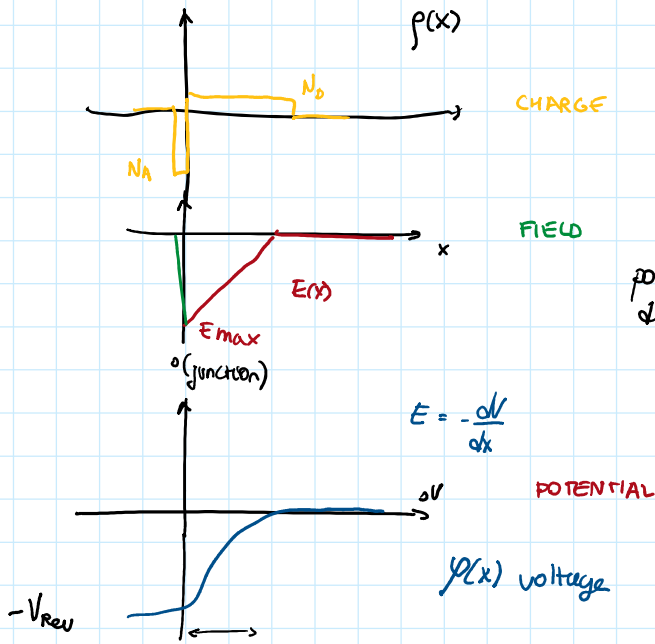
## PN JUNCTION



Detectors pn junction are reverse-biased



Detectors p-n junction are reverse-biased



potential difference and electric field

develops across depletion region width

$$E = -\frac{dV}{dx}$$

$$\frac{dE}{dx} = \frac{\rho}{\epsilon}$$

$$-\frac{d\rho}{dx} = E$$

potential - field - charge equations 1D

$$\frac{d^2\phi}{dx^2} = -\frac{\rho}{\epsilon}$$

$$\frac{d^2\phi}{dx^2} = -\frac{qN_D}{\epsilon}$$

Poisson equations 1D

Poisson equation

Assuming a unilateral junction - highly doped p+

$$\rightarrow E(x) = \int_0^x \frac{\rho}{\epsilon} dx = \frac{\rho}{\epsilon} (x - x_d)$$

$$|E_{max}| = \frac{qN_D}{\epsilon} x_{dep}$$

$$x_{dep} \sim x_n$$

$$V_{bias} = \int_0^{x_{dep}} -E dx = \sim \frac{1}{2} |E_{max}| x_d = \frac{1}{2} \frac{qN_D}{\epsilon} x_d^2$$

triangle area

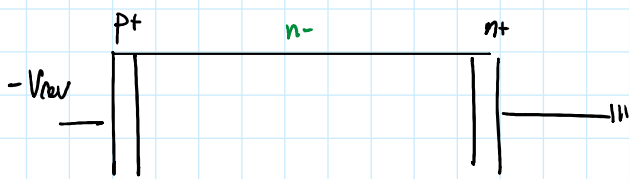
x detection is useful only if it happens in the depleted region

x detection is useful only if it happens in the depleted region

=> field move charges and creates signal

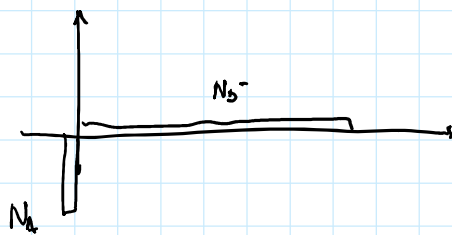
x if undepleted, the generated electron-hole pair shortly recombines

PN JUNCTION DETECTOR

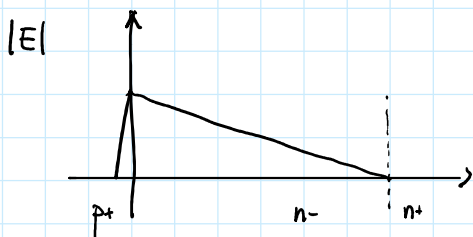


how to avoid neutral region problems: fully deplete region

how to fully deplete: dope n region to have  $x_{dep} = \text{width device}$



$$p_n = qN_D$$



$$\frac{dE}{dx} = \frac{qN_D}{\epsilon}$$

$$|E_{max}| = \frac{qN_D}{\epsilon} \cdot x_{dep} = \frac{qN_D W}{\epsilon}$$



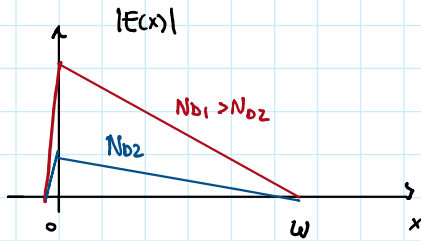
$$-\frac{d\phi}{dx} = E$$

$$V_{dep} = \int_0^{E_{max}} -E dx = \frac{1}{2} \frac{qN_D W^2}{\epsilon}$$

x neutral region removed, so possible to detect charge in whole device

x back side unwanted illumination is possible

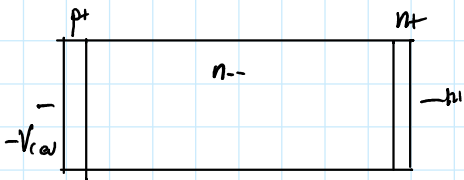
Balance between



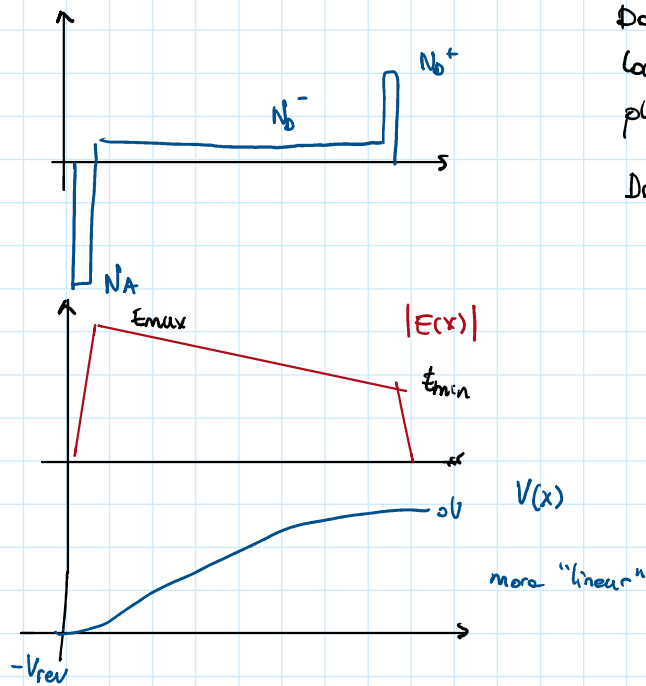
$$E_{max} = \frac{qN_0}{\epsilon} w \quad \text{critical field} \propto \text{doping}$$

- need low substrate doping avoiding large E field

FULLY DEPLETED PN DETECTOR



The fully depletion concept can be also improved: overdepleting - punch through of electric field



Doping in the n- region is so low that the electric field punches through to the n+ region

Depletion region reaches n+

$$V_{bias} = V_{dep} + \Delta V_{ob}$$

$$E_{min} = \frac{\Delta V_{ob}}{d} \quad (\text{offset of constant } E_{min})$$

$$|E_{max}| = |E_{min}| + |E_{dep}|$$

$$= \frac{\Delta V_{ob}}{d} + \frac{2V_{dep}}{d}$$

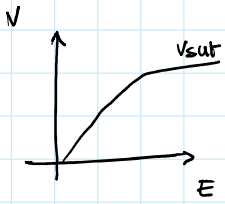
$$\hookrightarrow V_{dep} = \frac{1}{2} E_{dep} d$$

$$E(x) = E_{min} + \frac{qN_b}{\epsilon} (x_0 - x)$$

- x  $E_{min}$  helps avoiding near zero fields, resulting in a low velocity of generated e-h pairs and in a more probability of recombination

x  $E_{min}$  could be sized to ensure that the carriers are always in saturation velocity

As Drude model



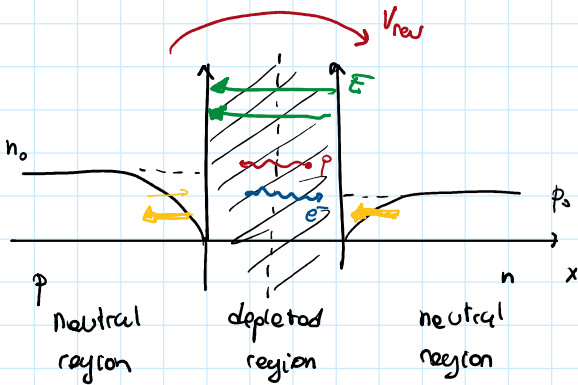
$$v = \mu E$$

$$v_{sat} = \mu E_{sat}$$

↑  
mobility

$$E_{min} = E_{sat} = \frac{v_{sat}}{\mu} \rightarrow 10^7 \text{ cm/V Si}$$

PUNCH THROUGH - OVERDEPLETED PN JUNCTION DETECTOR



minority carrier concentration in a reverse biased junction - long base diode

mass action law  
 $np = n_i^2$

min concentr  $n_{mi} = \frac{n_i^2}{N_A}$   
'region'

Current conserves through all device. current can be derived from neutral region

$$J_{tot} = J_{diff_n}(0) + J_{diff_p}(0)$$

by Ficks law

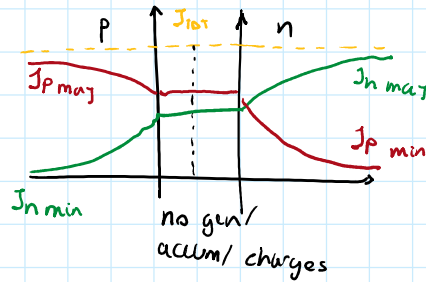
$$J_{tot} = +qD_n \left. \frac{dn}{dx} \right|_0 - qD_p \left. \frac{dp}{dx} \right|_0$$

$$J_{tot} = qD_n \cdot \frac{n_0}{L_n} + qD_p \cdot \frac{p_0}{L_p}$$

in long base diodes, lifetime of carriers is

$$L_n = \sqrt{D_n \tau_n} \quad (\text{from charge continuity eq.})$$

$$J_{tot} = q \left( n_{p0} \frac{L_n}{L_n} + p_{n0} \frac{L_p}{L_p} \right)$$



$$n(0) = n_0 e^{qV_0/kT} \quad \text{boltzmann}$$

$$n(x) = n_0 \left( e^{qV_0/kT} - 1 \right) e^{-x/L_n} + n_0$$

when  $V_0 = -V_{rev}$   
(reverse biased diode)  
 $n(0) \approx 0$

$$\rightarrow n(x) = n_0 (1 - e^{-x/L_n})$$



LEAKAGE REVERSE CURRENT | NO GENERATION IN DEPLETION REGION

$$J_{TOT} = q \left( n_{p0} \frac{L_n}{\tau_n} + p_{n0} \frac{L_p}{\tau_p} \right) + \frac{q n_i W_{dep}}{2\tau}$$

obtain  $W_{dep}$  from poisson eq.

neutral region contribution

Generation/Recombination processes depletion region

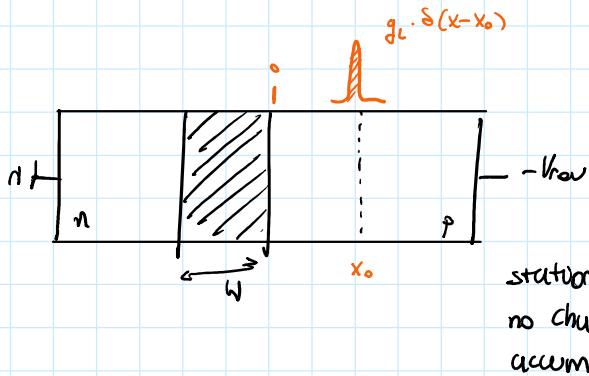
LEAKAGE REVERSE CURRENT

with a fully depleted diode, only contribution to the current are generation / recombination processes in depleted region

$$J_{leak} \approx q \frac{n_i}{2\tau} W_{max} (w) \quad 1.45 \cdot 10^{10} \text{ cm}^{-3}$$

FULLY DEPLETED JUNCTION - REVERSE LEAKAGE CURRENT

LEAKAGE CURRENT



$$\frac{dn(x)}{dt} = \frac{1}{q} \frac{dJ}{dx} + (G-R)$$

cont eq

$$0 = \frac{1}{q} \frac{d}{dx} (q D_n \frac{dn}{dx}) + G-R$$

continuity equation in p neutral region

$$D_n \frac{d^2 n(x)}{dx^2} - \frac{n(x) - n_0}{\tau_n} = -g \delta(x-x_0)$$

$$\frac{1}{q} \frac{dJ}{dx}$$

R

G

recombination rate

We can obtain the current generated by this  $\delta$  like collection solving the continuity equation in point  $x=0$  as before

solving the continuity equation in point  $x=0$  as before

$$J_n = q D_n \frac{dn(0)}{dx} = q D_n \frac{n_0}{L_n} - \underbrace{q g_L e^{-x/L_n}}_{\text{circled}}$$

(assuming  $\frac{q V_{rev}}{kT} \sim 0$ )

from this term

we obtain that generated current is not detectable when collection happens in depleted region or at  $x < L_n$  in the neutral region

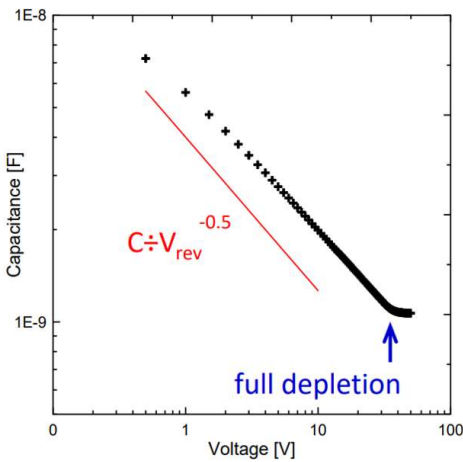
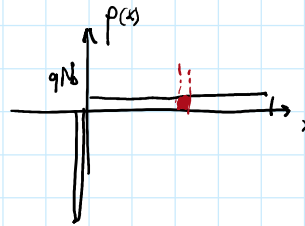
Why??

From differential equation solution

NEUTRAL REGION DETECTION

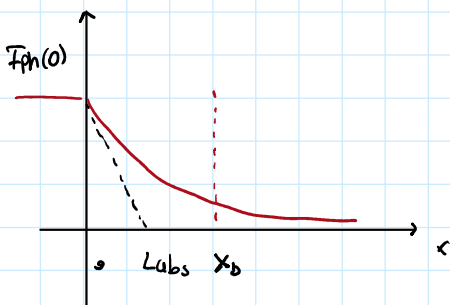
$$C_D' = \frac{dq}{dV} \bigg|_{V=V_R} \cdot \frac{1}{A} = \frac{\epsilon_0 \epsilon_r}{w_{dep}(V_R)} \sim \frac{\epsilon_0 \epsilon_r}{x_n(V_R)} \propto \sqrt{\frac{N_D}{V_R}}$$

unilateral junction



Capacitance goes down with increase of  $w_{dep}$

DEPLETED REGION CAPACITANCE



$$J_{ph}(x) = J_{ph}(0) e^{-\mu_p x}$$

attenuation / absorption length are

DETECTION / QUANTUM EFFICIENCY

$$\eta = \frac{I_{ph}(0) - I_{ph}(x_0)}{I_{ph}(0)}$$

$$\eta = 1 - e^{-\mu p x} = 1 - e^{-x_0/L_{abs}}$$

We should size

$x_0 \ll N_{\text{few}} \ll L_{\text{abs}}$

=> so all particles absorbed are generating current signal

depletion region  
dependent on  
electronics of  
detector  
Bias  $N_D$

size  
←

ABSORPTION LENGTH  
light - material  
dependent on  
 $\mu$   $p$   
material

DEPLETED REGION SIZING

HIGH RESISTIVITY LOW DOPING DETECTORS

low doping

=> allows for fully depleted junctions without exceeding high  $E_{\text{max}}$

(?)  
true?

RESISTIVITY DEPENDS ON DOPING

$$\rho = \frac{1}{q n \mu}$$

res

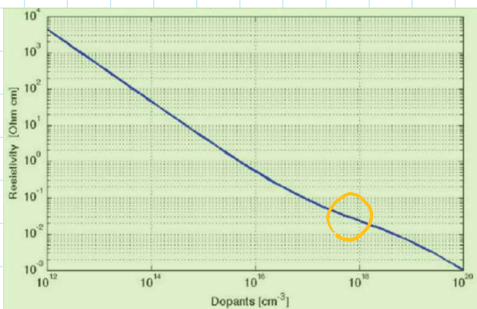
$N$  direct dependence on doping

$N \uparrow \quad \mu \downarrow$   
more carriers

$\mu$   $N \uparrow \quad \mu \downarrow \quad \rho \uparrow$

mobility lowers as there are more scattering effects

2nd order effect



RESISTIVITY

low doping limit

← ... as under  $10^{12} \text{ cm}^{-3}$

low doping limit

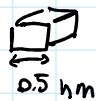
↳ we cannot realistically go under  $10^{12} \text{ cm}^{-3}$  of doping

$$(n_i = 1.45 \cdot 10^{10} \text{ cm}^{-3})$$

↳ in perspective  $10^{12} \text{ cm}^{-3}$  is an extremely pure silicon crystal - too pure to obtain!

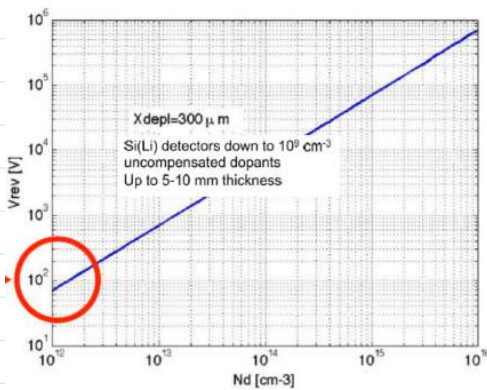
$10^{12} \text{ cm}^{-3} \rightarrow 1 \cdot \mu\text{m}^{-3}$  dopant concentration  
 $\Rightarrow$  ~~1 dopant per  $\mu\text{m}^3$~~

In  $1 \mu\text{m}^3$  there are



$$\frac{1 \mu\text{m}^3}{(0.5)^3 \cdot \text{nm}^3} = 8 \cdot 10^9 \text{ silicon lattice units}$$

LOW DOPING LIMIT

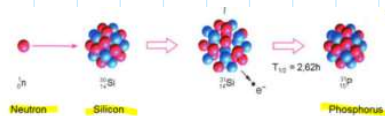


Required to keep reasonable voltage / field values!

$$E_{\text{max}} = \sqrt{\frac{2q}{\epsilon} N_0 V_{\text{rev}}}$$

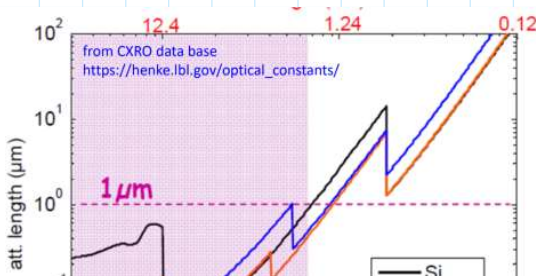
↳ corollary: high purity silicon detector grade is produced using

NTD (neutron transmutation doping)



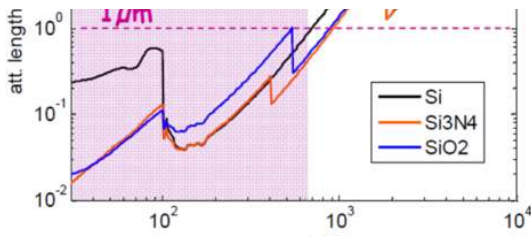
(not using implantation or deposition of dopants)

LOW DOPING NECESSITY



Attenuation Length - Energy (contrary to visible light)

Attenuation length increases with energy in X RAYS



energy in X RAYS

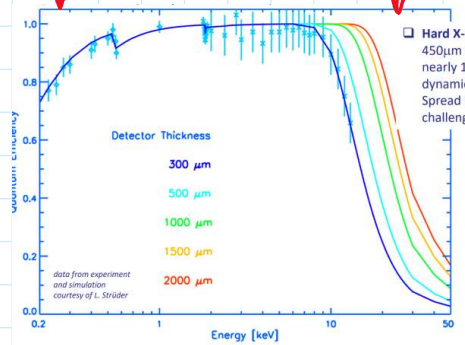
=> more energetic photons travel further in the device

x too small Labs photon is stopped too soon in dead layer or a low field region

x too large Labs photon may not interact with the detector (pass through)

SOFT X

HARD X



ABSORPTION LENGTH W ENERGY

x high energy inefficiency can be solved only through a deeper detector

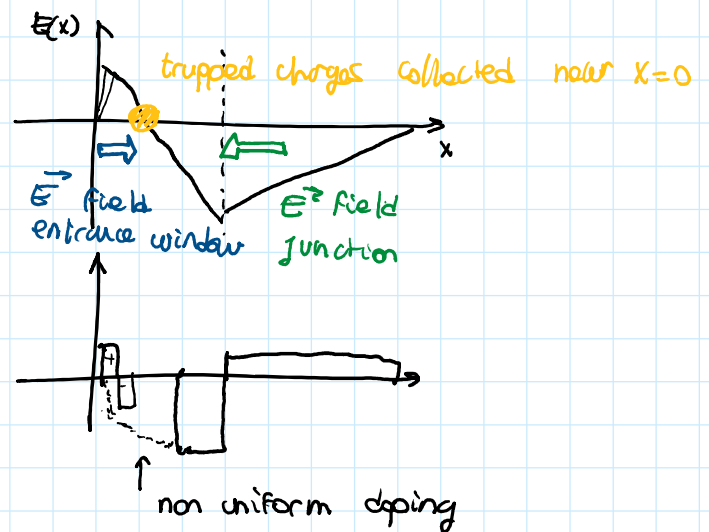
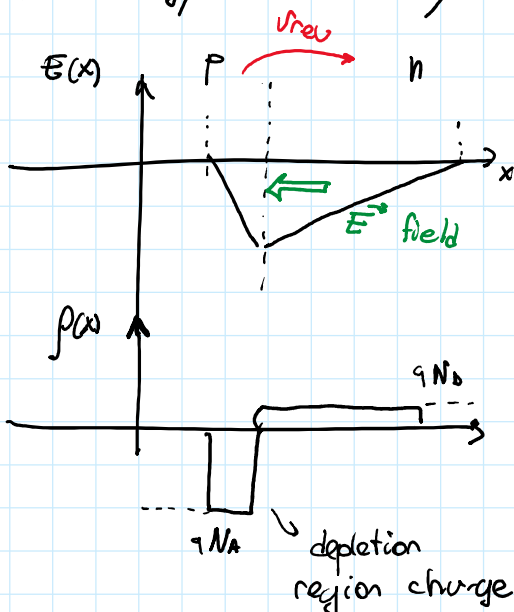
or using high z detector (higher stopping power)

technology process limit

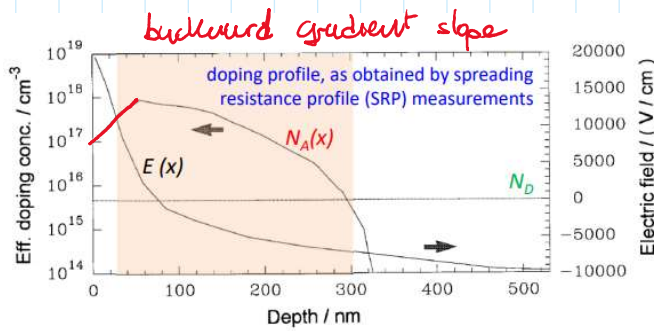
(wafer thickness)

(2mm)

x low energy inefficiency : ENTRANCE WINDOW



the small backward E field is created due to doping process - non idealities



rev. Fick's law

$$J_{diff} = qD_n \frac{dp}{dx}$$

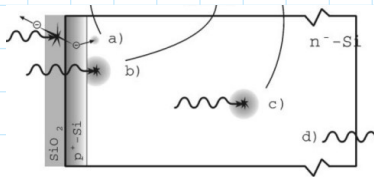
$\uparrow$  non-0

=> a

sort of junction is created

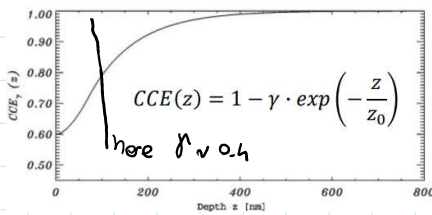
=> contrast electric field

other imperfections are:



• oxide generated on wafer

the incomplete charge collection can be modeled as



empirically

$\gamma = 0.09$  : maximum lost charge fraction  
 $z_0 = 100 \text{ nm}$

$$CCE(0) = 1 - \gamma$$

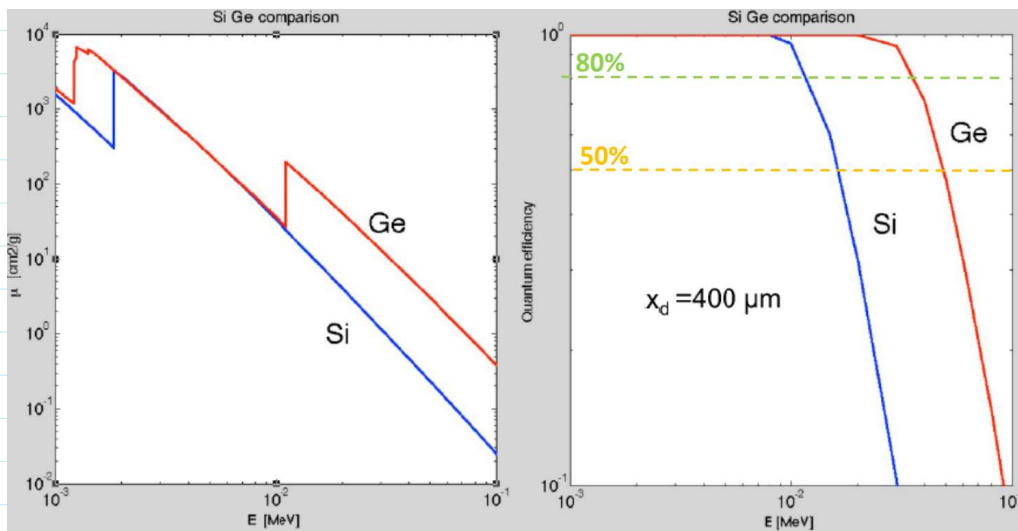
### ENTRANCE WINDOW

High Z detector are useful for their higher absorption coefficient ( $\mu$ )

=> they are able to capture more efficiently photons

=> same thickness (process limited) -> higher detectable energy

Z: atomic number



- for example, Germanium suitable for  $\gamma$ -rays

Beware however for bandgap:

- lower bandgap means higher leakage current!

=> this detectors (lower bandgap) have a higher 1kg current

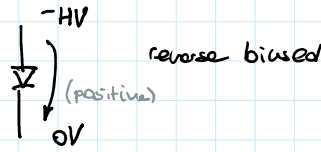
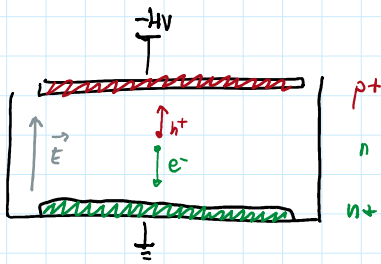
=> higher noise

to mitigate, they must be cooled extensively (77K)

# 2D DETECTORS

Tuesday, May 14, 2024 11:32 AM

## PIN DIODE DETECTOR



$n^+ \text{ layer (electron collector)}$

$$V_{bi} = \frac{kT}{q} \ln \left( \frac{N_A N_D}{n_i^2} \right)$$

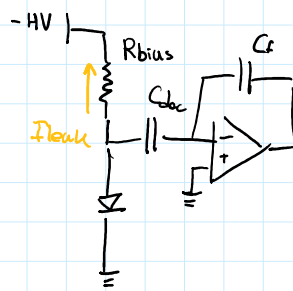
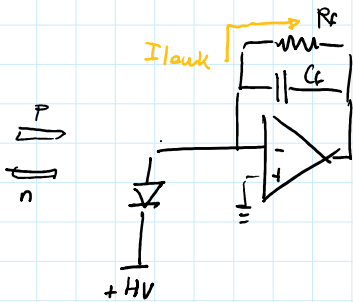
Capacitance

$$d_{dep} = \sqrt{\frac{2 \epsilon \epsilon_0 (N_A + N_D)}{q N_A N_D} (V_{bi} - V_{rev})} \Rightarrow C_{dep} = \frac{\epsilon_0 \epsilon_r A}{d}$$

$\uparrow$  depletion region width

## PIN DETECTOR

### DETECTOR BIASING



DC: leakage current

$$\Rightarrow V_{out} = I_{leak} R_f$$

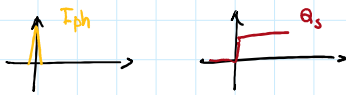
DC: leakage current

$R_{bias}$  big  $\Rightarrow$  tuned to  $I_{leak}$

$\Rightarrow$  decoupled from  $V_{out}$

$$I_{leak} = -J_0 A$$

AC PULSE: charge detection



AC PULSE

$$V_{out} = \frac{I_{ph}}{SCF} = \frac{Q_{ph}}{C_f}$$

$$V_{rev} = HV - J_0 A R_{bias}$$

$$\Rightarrow V_{out}(t) = I_{ph} \left( \frac{R_f}{1 + s R_f C_f} \right) \sim I_{ph} \frac{1}{s C_f} = \frac{Q_{ph}}{C_f}$$

### AC-DC BIASING

The detector (that can be divided in strips) is biased through contact

regarding AC biasing (with capacitor on diode) the biasing can be made in 2 ways

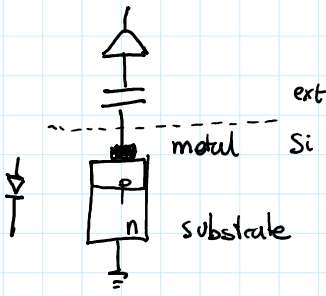
DC\* COUPLING

AC COUPLING

(\*no later impedance ...)

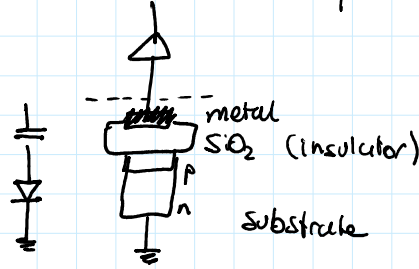


DC COUPLING



- external capacitor
- external bias (as seen before)

AC COUPLING (\*see later implementation)

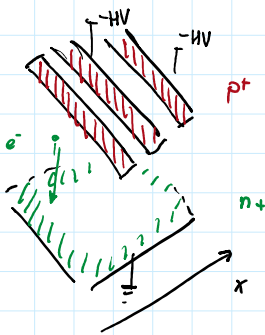


- capacitor integrated and interleaved with pn junction
- must be biased internally (diode cathode cannot be reached from ext to bias HV)

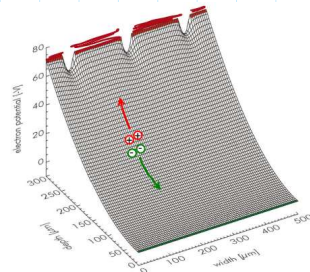
EXTERNAL-INTERNAL COUPLING

POSITION SENSING WITH STRIPS

1D DETECTOR STRIP



↑ hν enters in one of the strips along x axis ⇒ 1D position detection

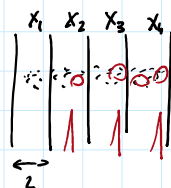


~ fully depleted diode

PIN DIODE FOR 1D POSITION DETECT.

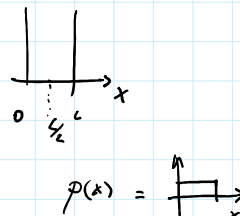
1D data can be extracted to get the position of the photon hit

2) Digital readout when  $L \gg \sigma_x$  charge ( $L$  strip  $\gg$  charge cloud)



$$\sigma_x^2 = \frac{1}{L} \int_{-L/2}^{+L/2} x^2 dx = \frac{L^2}{12}$$

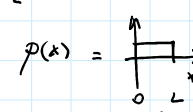
↑ quantization noise



$\langle X \rangle = X_i \text{ center}$   
 $\uparrow$   
 resolution  $\Rightarrow$  strip width

↑ quantization noise

$\frac{1}{L}$



$$\sigma^2 = E(x^2) - [E(x)]^2$$

$$= \frac{1}{L} \int_0^L x^2 dx$$

2) Interpolation of charge cloud  
 $L \ll \sigma_x \text{ charge}$

$$\langle x \rangle = \frac{x_1 Q_1 + x_2 Q_2}{Q_{\text{TOT}}} = \frac{x_1 Q_1 + (x_1 + L) Q_2}{Q_{\text{TOT}}}$$

$$= x_1 + \frac{Q_2}{Q_1 + Q_2} L$$



"infinite" resolution

$\Rightarrow$  dependent on SNR we use to read  $Q_1, Q_2 \dots$

RESOLUTION CAN BE BETTER THAN STRIP WIDTH

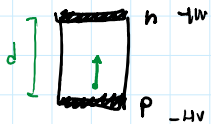
$$\sigma_x^2 = \frac{ENC^2}{Q^2} L^2 = \frac{L^2}{(SNR)^2}$$

- use bigger strips
- low SNR
- $\Rightarrow$  better result!

for cloud size, use brownian (thermal) motion

max drift time  $= \tau_n = \frac{E}{N_n q N_D}$   $\vec{v} = \mu \vec{E}$

max drift time, in detector



$$\tau_{\text{drift}} = \frac{d}{\vec{v}} = \frac{d}{\mu E} = \frac{d}{\mu q N_D d / E} = \frac{E}{\mu q N_D}$$

$$E = \int \rho_E dx \text{ (assuming const. doping)}$$

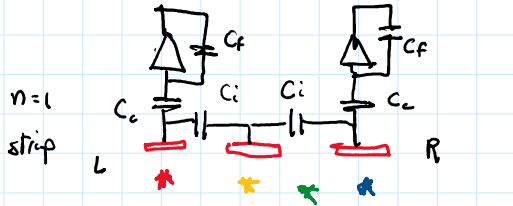
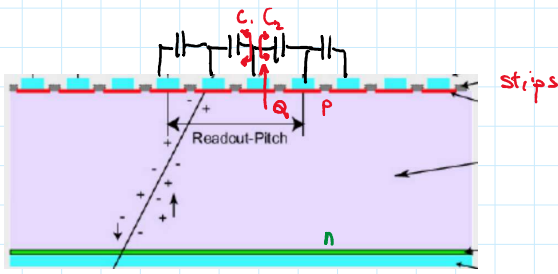
$$= \rho_E d = \frac{q N_D d}{E}$$

$$\Delta X_{\text{diff}} = \sqrt{2 D n \tau_n} \rightarrow \text{brownian motion Gaussian FWHM}$$

[https://en.wikipedia.org/wiki/Fick%27s\\_laws\\_of\\_diffusion#Example\\_solution\\_2:\\_Brownian\\_particle\\_and\\_mean\\_squared\\_displacement](https://en.wikipedia.org/wiki/Fick%27s_laws_of_diffusion#Example_solution_2:_Brownian_particle_and_mean_squared_displacement)

CHARGE READOUT METHODS

interpolation readout can be done using capacitances  
- capacitive coupling



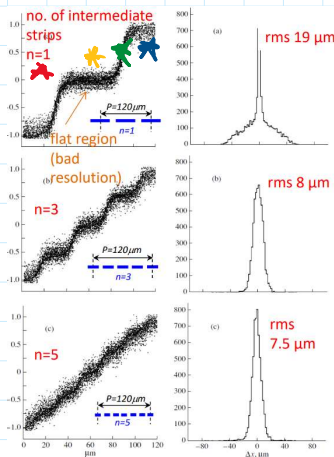
$$\gamma = \frac{Q_R - Q_L}{Q_R + Q_L}$$

charge shared equally between R L

- ✓ charge all on R
- ~ charge all on L

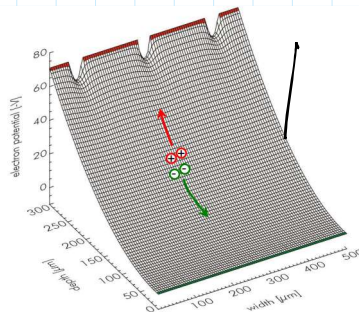
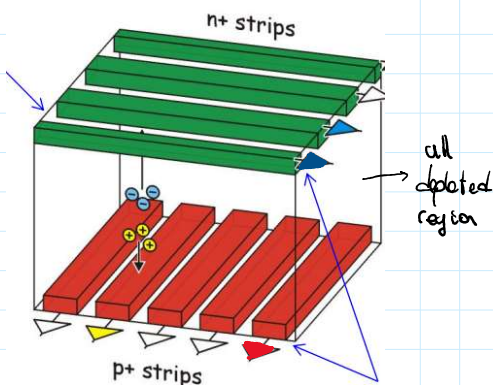
(consider as a virtual ground R/L connection post  $C_c$  approximation, as  $C_c$  big??)

charge sharing happens between the strips (interpolation regime)



### INTERPOLATION READOUT

#### 2D DETECTOR STRIP



We can exploit having 2 different carriers to have 2D position sensing, using strips on both sides

#### 2D STRIP DET.

Introducing  $n+$  strips causes a problem. As oxide is always deposited onto the surface (Required by process)

Introducing  $n^+$  strips causes a problem. As oxide is always deposited onto the surface (Required by process)

$\text{SiO}_2$  contains  $\oplus$  trapped charges

$\Rightarrow$  INSULATION ISSUE

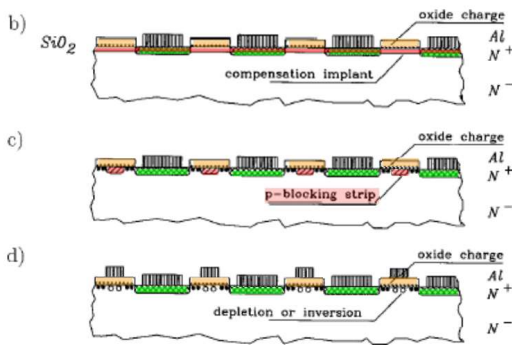


(+) trapped charges attract (-) electron carriers

This creates two main problems

- carriers (electrons) remain trapped under strip-to-strip spaces
- the strips are no more insulated as a channels forms between strips (few  $\sim$   $\mu\text{m}$ )  
 $\rightarrow$  this may favor spread of carriers and lower position precision

Possible solutions are:



free pt spray: replace electron (-) accumulation with  $\text{Na}$  ( $\oplus$  fixed charge)

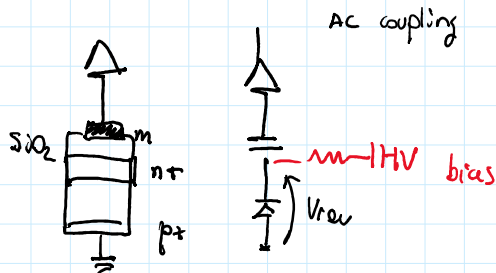
Same principle, but pt is implanted and not on all surface

A field effect (like in MOS) can be used to invert the layer, removing (-) accumulation

(on pt strips we don't have this problem, as they are interfused with  $n^-$  substrate, so the inter-strip space is depleted - no free carrier to accumulate underneath)

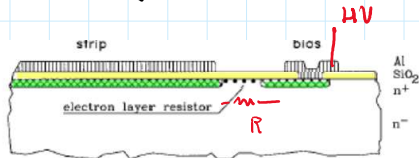
$\text{SiO}_2$  INSULATION ISSUE

\* AC BIASED strips

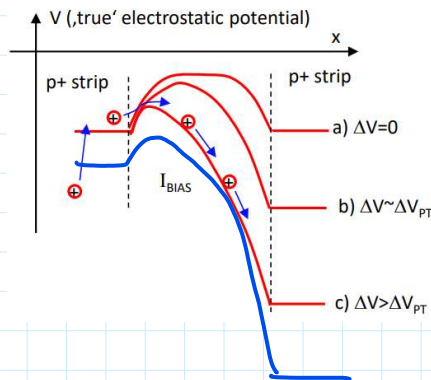
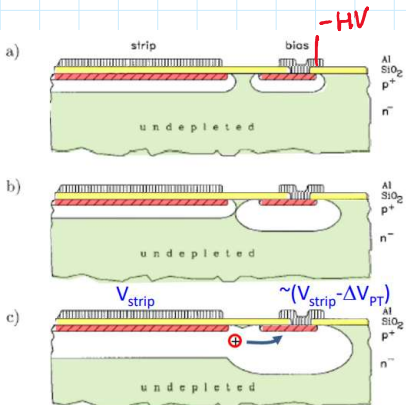


1) POLYSILICON RESISTOR : connected with n+ an brought out to a metal contact

2) ELECTRON ACCUMULATION LAYER RESISTANCE by exploiting SiO<sub>2</sub> positive trapped charges, creating an accumulation (resistive) layer



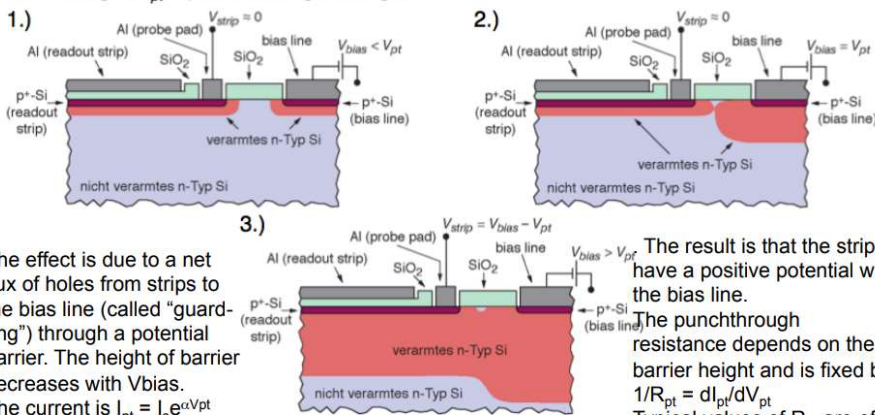
3) P+ BIAS/V BY PUNCH THROUGH (on p+ strips - other side)



when punch through reached, the strips are like "shorted out" (zener diode like)

=> so pulling further the BIAS voltage drags down the strip voltage

Punch through effect: Figures show the increase of the depletion zone with increasing bias voltage ( $V_{pt}$  = punch through voltage).



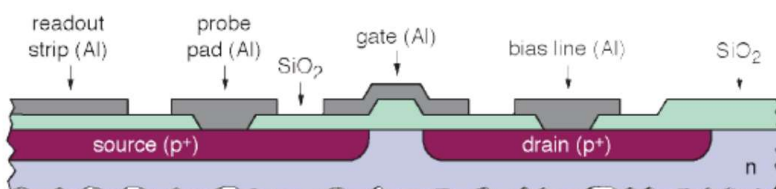
The effect is due to a net flux of holes from strips to the bias line (called "guard-ring") through a potential barrier. The height of barrier decreases with  $V_{bias}$ . The current is  $I_{pt} = I_s e^{\alpha V_{pt}}$

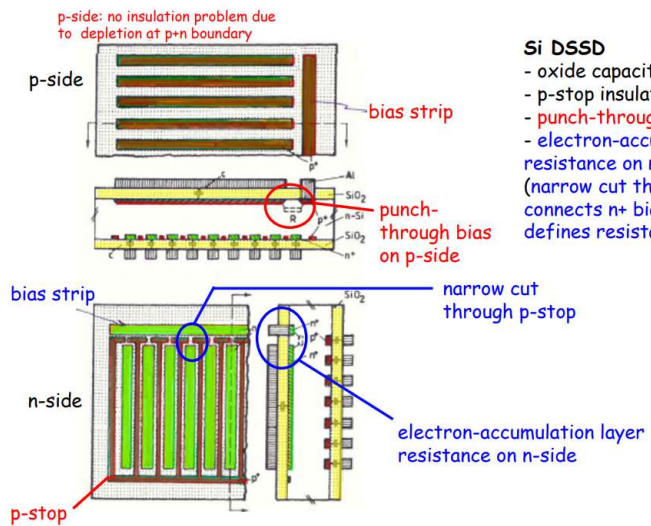
The result is that the strips have a positive potential wrt the bias line. The punchthrough resistance depends on the barrier height and is fixed by  $1/R_{pt} = dI_{pt}/dV_{pt}$ . Typical values of  $R_{pt}$  are of O(1-10 GOhm)

Advantage: No additional production steps required.

4) FOXFET

Create a resistive channel using FET gate. The resistance is variable and can be modulated



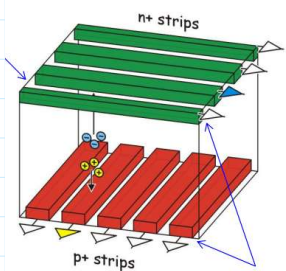


**Si DSSD**

- oxide capacitance
- p-stop insulation for n+ strips
- punch-through bias on p-side
- electron-accumulation layer resistance on n-side (narrow cut through p-stop connects n+ bias strip and defines resistance value)

**BIASING METHODS**

**STRIP DETECTOR TOPOLOGIES**



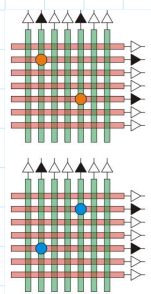
already seen  
 DSSD Double side Silicon Drift detector

**PRO**

- simple
- few readout electronics

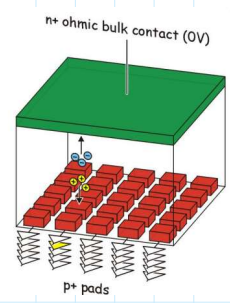
**CON**

- n+ strip insulation problem
- simultaneous event corruption  
 ↳ not suitable for high occupancy measurements



**DSSD**

P pixel on N substrate (p or n)



**PRO**

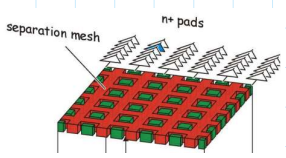
- solves n+ strip problems
- solves sim events

**CON**

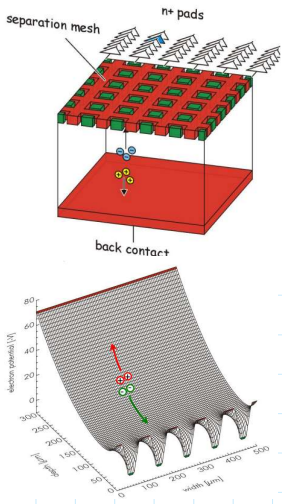
- n<sup>2</sup> electronics readout instead of 2n

+ power consumption  
 + interconnections

**p on n**



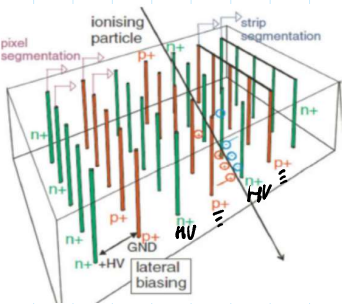
n pixels but with p+ mesh to avoid accumulation problems on n strips



n pixels but with p+ mesh to avoid accumulation problems on n strips

same as P on N

N on N



3D detector

- lower voltage need (closer strips)
- larger capacitance
- signal amplitude and charge collection can be chosen separately

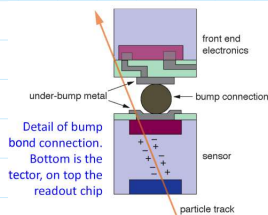
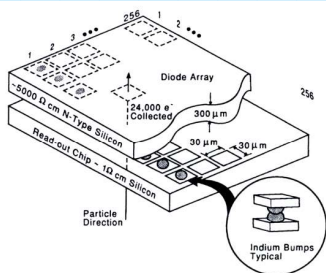


BEFORE, ON PLANAR DETECTOR  
 more deep => more charges generated + repassing  
 more deep  
 => more charge collection  
 distance (more cloud  $\Delta x$ , collection time) (bad!)

TRADE OFF

3D detector  
 deeper => charges more generated + repassing  
 charge collection  
 distance fixed by distance between n and p fingers

3D DETECTORS



connection method for a strip detector to a readout electronics chip

pro:

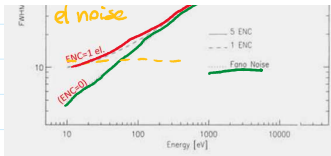
- possibility to choose different purity grade silicon \$
- different process / technology too!

HYBRID PIXEL

---







(intrinsic) contributions  
contribution

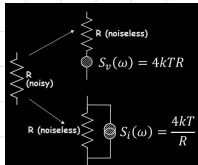
$$\sigma_n^2 = \frac{F(E)}{\omega} + \left(\frac{ENC}{q}\right)^2$$

**FANO - ELECTRONICS NOISE CONTRIB.**

Noise in electronics components.

Note that the given power spectral densities are UNILATERAL

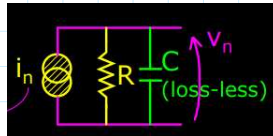
**RESISTOR**



$$S_{v_o}(f) = 4kTR$$

$$S_{i_o}(f) = \frac{4kT}{R}$$

**CAPACITOR (LOSSY)**



shunt resistance across capacitance - due to dielectric relaxation

admittance

$$\epsilon_r = \epsilon_r' - j\epsilon_r'' \quad \tan \delta = \frac{\epsilon_r''}{\epsilon_r'}$$

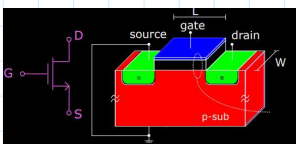
$$Y = j\omega(\epsilon_r' - j\epsilon_r'') \frac{\epsilon A}{d} = \underbrace{j\omega C}_{\text{complex}} + \underbrace{\omega C \tan(\delta)}_{\text{real}}$$

$$S_i(\omega) = \frac{4kT}{R} = 4kT \cdot \omega C \tan(\delta)$$

$$S_v(\omega) = 4kT \frac{\tan(\delta)}{\omega C} \quad ?$$

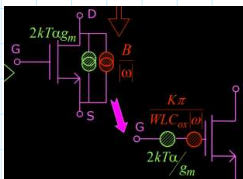
=> lossy capacitors introduce 1/f noise

**MOSFET**



They introduce white and 1/f noise.

1/f contribution is often significant and must be addressed

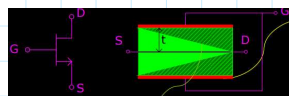


$$S_{w_o}(f) = 4kT\alpha g_m \quad (A^2/Hz)$$

Sat-region  $\alpha \approx \frac{2}{3} \approx 2$

$$S_{w_o}(f) = 4kT/R_{cha} \quad (A^2/Hz)$$

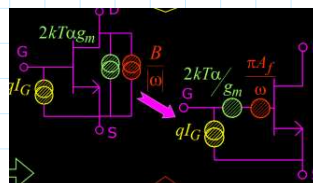
**JFET**



Junction FET exploit a pn junction depletion region to close the channel between source and drain.

this could using a gate capacitor, introducing oxide and trapped charges giving rise to 1/f noise

However pn junctions add shot noise, due to leakage current between gate and s/d



$$S_{w_o} = 4kT\alpha g_m \quad (A^2/Hz)$$

$S_{w_0}(f) = 4kT/R_{cha} \quad (A^2/Hz)$   
ohmic resistor

$S_i = \frac{S_w \cdot \omega_c}{\omega} \quad (A^2/Hz)$   
↓ (doing  $\frac{S_i}{\omega^2}$  → physics on gate)

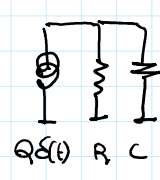
$S_v = \frac{k}{C_{ox}WL} \cdot \frac{1}{\omega} \quad (V^2/Hz)$

$S_{w_0} = 4kT \cdot g_{m0} \quad (A^2/Hz)$

$S_{1/f} = \frac{S_w \cdot \omega_c}{\omega} \quad (A^2/Hz)$

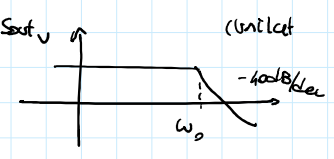
$S_s = 2qI \quad (A^2/Hz)$   
shot  
between drain gate / drain

NOISE IN DEVICES



$S_v = S_i \cdot \left( \frac{R}{1+sRC} \right)^2 = \frac{4kTR \cdot R^2}{R (1+\omega^2 R^2 C^2)}$

$= \frac{4kTR}{(1+\omega^2 R^2 C^2)}$



$\sigma_v^2 = \int_{-\infty}^{+\infty} S_{v,0}(f) df = \text{noise BW} = 4kTR \cdot \frac{\pi}{2} \cdot \frac{1}{2\pi RC} = \frac{kT}{C}$

noise charge on capacitor

$C = \frac{Q}{\Delta V}$

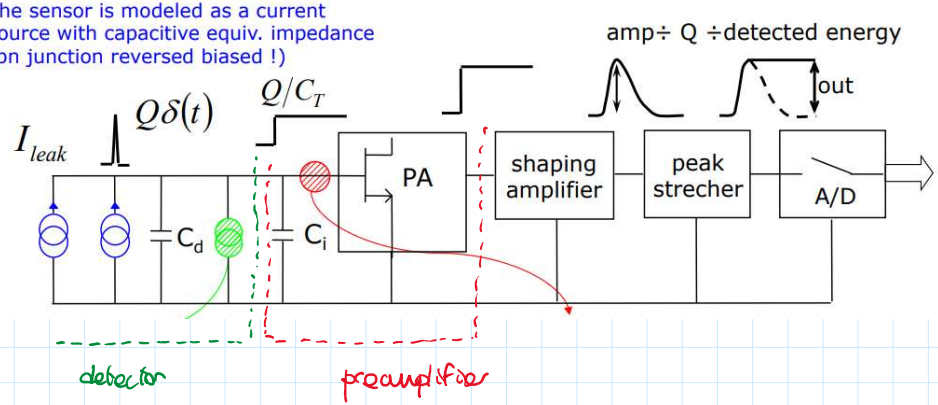
$\sigma_c^2 = (C^2) \cdot \sigma_v^2 = KTC$  independent on R

because proportionally increase noise and inverse proportionally decrease BW, does not impact on noise MSU

KTC RC FILTER NOISE

NOISE PERFORMANCE MEASUREMENT SYSTEM TOPOLOGY

The sensor is modeled as a current source with capacitive equiv. impedance (pn junction reversed biased !)



signal: can be modeled as a pulse like current delivering

typically a common source FET

can be measured as a pulse like current delivering a fixed amount charge

common source FET

- $I_{leak}$ : junction leakage
- $C_d$  junction depletion region capacitance
- noise: shot noise from leakage current

•  $C_T$  gate capac.

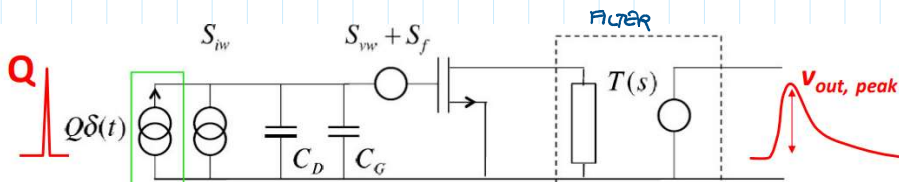
•  $\rightarrow$  series voltage  
 $\downarrow$  noise

$$\frac{2kT\alpha}{g_m} + \frac{1}{2} \frac{A_f}{f} \left[ \frac{V_s}{f} \right]$$

(bilateral)

### ELECTRONICS OVERVIEW

pay attention, in following calculations we use bilateral spectral densities



Ampere  
 $F(s) = Q$   
 (delta  $\Rightarrow$  constant in F)  
 (step  $\Rightarrow$  integral of  $\delta$   
 $\Rightarrow 1/s \cdot$  constant)

$$I_{drain} = Q \cdot \frac{1}{s(C_D + C_G)} \cdot g_m$$

$$V_{out}(s) = Q \cdot \frac{1}{s(C_D + C_G)} \cdot g_m \cdot T(s) = Q \cdot H(s)$$

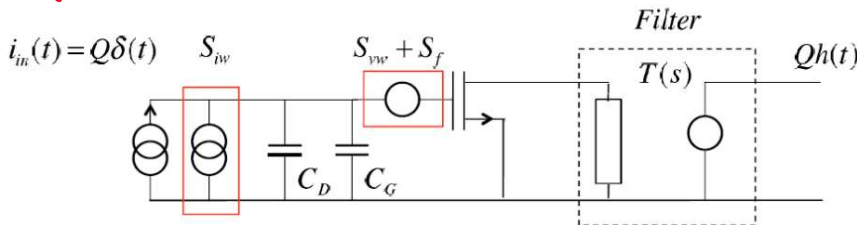


$$V_{out}(t) = \frac{Q}{(C_D + C_G)} g_m \cdot \mathcal{L}^{-1} \left( \frac{1}{s} T(s) \right)$$

$\uparrow$  filter

### SIGNAL OUTPUT

using bilateral spectral densities: unilateral / 2



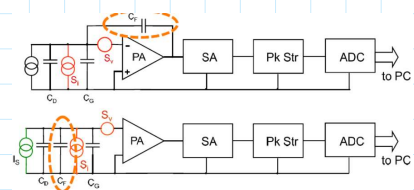
$S_{iw}$ : detector shot noise =  $qI_{leak}$

$S_{wv}$ : input referred FET white noise =  $\frac{2kT\alpha}{g_m}$

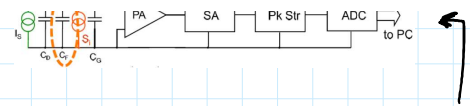
$S_f$ : input referred FET  $1/f$  noise =  $\frac{1}{2} \frac{A_f}{f} = \frac{\pi A_f}{\omega}$

noise power spectral density

$\rightarrow$  choose amplifier



noise power spectral density



$$C_D = C_{stray} + C_{detector} + C_{feedback}$$

↗ choose amplifier feedback of ANY why →

For the SNR calculation (NOT SIGNAL OR NOISE ONLY SNR)  $C_D$  can be considered parallel to detector

We can then compute the power spectral density at the output

$$S_{vout}(\omega) = \left( \frac{2kT\alpha}{g_m} + \frac{\pi A_i}{\omega} + g_{Fk} \cdot \frac{1}{\omega^2(C_D + C_G)^2} \right) g_m^2 |T(j\omega)|^2$$

to get the noise power (variance of the noise)

$$\sigma_{n_{out}}^2 = \int_{-\infty}^{+\infty} S_{vout} df = \frac{1}{2\pi} \int_{-\infty}^{+\infty} S_{vout} d\omega \quad d\omega = d(2\pi f)$$

To quickly evaluate signal impact, it is common to input refer the noise sources / power spectral densities

=> input referred current (equal to signal)

$$S_{vout} = \left[ \left( \frac{2kT\alpha}{g_m} + \frac{\pi A_i}{\omega} \right) \cdot \omega^2 (C_D + C_G)^2 + g_{Fk} \right] |H(s)|^2$$

input referred power spectral densities

NOISE OUTPUT

Signal to noise ratio is computed

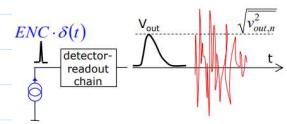
$$\left( \frac{S}{N} \right)^2 = \frac{Q^2 \frac{g_m^2}{(C_D + C_G)^2} \cdot \text{flux}(\alpha^2 |T(s)/s|)}{\frac{1}{2\pi} \int_{-\infty}^{+\infty} S_{vout}(\omega) d\omega} \sim 1 \quad \begin{matrix} V_{out}^2 \text{ (peak)} \\ \sigma_{out}^2 \end{matrix}$$

$$\left( \frac{S}{N} \right)^2 = \frac{Q^2 \frac{g_m^2}{(C_D + C_G)^2}}{\frac{1}{2\pi} \int S_{vout}(\omega) d\omega}$$

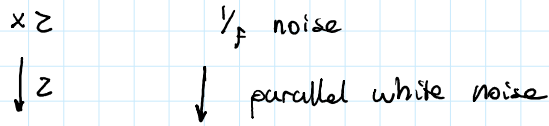
We can define the EQUIVALENT NOISE CHARGE figure of merit

if we impose a  $S/N = 1$  we can find the minimum detectable signal at the input finding the needed signal charge to equal the noise amplitude

$$1 = \frac{(ENC)^2 \cdot \frac{g_m^2}{(C_D + C_G)^2}}{\frac{1}{2\pi} \int S_{vout}(\omega) d\omega}$$







while the  $A_1, A_2, A_3$  term are dependent only on the shaper output "shape", not on the absolute time of the shape

SHAPER IMPACT

the filter has so an impact on SNR, as its filtering action must enhance SNR

Let's consider only white noise at first

$$SNR = \frac{Q^2}{\frac{2kT\alpha}{gm} C_T^2 \int_{-\infty}^{+\infty} \omega^2 |h(t)|^2 dt + qF \int_{-\infty}^{+\infty} |h(t)|^2 dt}$$

$\uparrow$  derives from  $k_{ww}(\omega) \Rightarrow \int h(t)^2 dt$   
 constant terms out integral

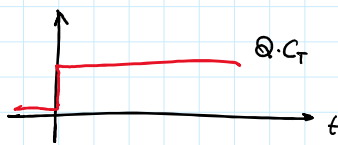
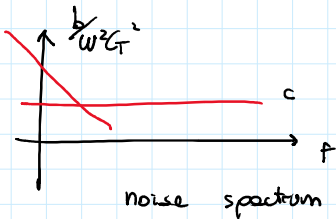
----- series white ----- parallel white

NOISE power spectral density



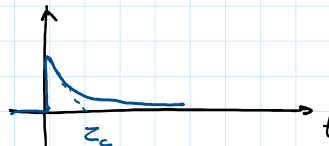
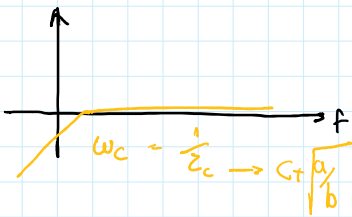
$\omega_c = \frac{1}{z_c} = z_c = C_T \sqrt{\frac{a}{b}}$  noise corner time constant

after capacitance  $\cdot \frac{1}{\omega^2 C_T^2}$



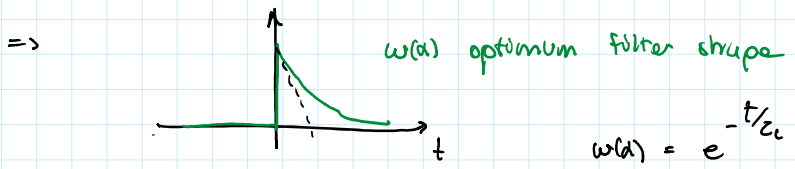
signal time shape impulse to step (integration on capacitance)

following optimum filter theory - whitened noise



signal shape after whitening filter

and build filter on signal shape



$$w(f) = e^{-f/z_c}$$

$z_c$  cutoff point

$$= C_T \sqrt{\frac{a}{b}}$$

OPTIMUM SHAPING ON WHITE NOISE

If 1/f noise is present and not negligible, unfortunately we can't build an optimum filter

We can size and shape the filter optimizing the output

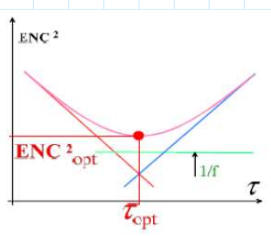
<p><b>• Indefinite cusp</b> (optimum shape for white noises)</p> <p><math>A_1=1</math> <math>A_2=2/\pi</math> <math>A_3=1</math> <math>F=1</math> worsening factor</p>	<p><b>• RC-CR</b></p> <p><math>A_1=1.85</math> <math>A_2=1.18</math> <math>A_3=1.85</math> <math>F=1.359</math></p>
<p><b>• Triangular</b> (optimum shape for white voltage noise and finite measurement time)</p> <p><math>A_1=2</math> <math>A_2=(4\ln 2)/\pi</math> <math>A_3=2/3</math> <math>F=1.075</math></p>	<p><b>• Trapezoidal</b></p> <p><math>A_1=2</math> <math>A_2=1.38</math> <math>A_3=5/3</math></p> <p>◦ The "flat-top" regions contributes only to <math>A_2</math> and <math>A_3</math>. ◦ <math>A_1</math> is equal to the triangular case having the same leading and trailing edges</p>
<p><b>• Pseudo-Gaussian (4th order)</b></p> <p><math>A_1=0.51</math> <math>A_2=1.04</math> <math>A_3=3.58</math> <math>F=1.165</math></p>	

OTHER FILTERS SHAPES

The shaping time can be optimized following the noise contributions. (when we can't follow the optimum filter)

$$ENC^2 = (C_D + C_G)^2 a \frac{1}{z} A_1 + (C_D + C_G)^2 c A_2 + b z A_3$$

series white                      series 1/f                      parallel white



$$z_{opt} \Rightarrow ENC^2_{series\ white} = ENC^2_{par\ white}$$

$$z_{opt} = \sqrt{\frac{A_1}{A_3}} \sqrt{\frac{a}{b}} (C_D + C_G)$$

$$ENC^2_{opt} = c(C_D + C_G)^2 A_2 + 2 \cdot \sqrt{A_1 A_3} \sqrt{ab} (C_D + C_G)^2$$

OPTIMAL SHAPING TIME W/ 1/f NOISE

Input transistor parameters (preamplifier) influence the SNR via

$$ENC^2 = ((C_D + C_G)^2 a \frac{1}{z} A_1 + (C_D + C_G)^2 c A_2 + b z A_3$$



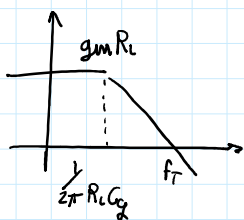
$$ENC^2 = (C_G + C_D)^2 \left[ \frac{1}{Z} A_1 + (C_D + C_G)^2 C A_2 + b Z A_3 \right]$$

$$a = S_{v_b \text{ white}} = \frac{2kT\alpha}{g_m}$$

$$c = \pi A_f \leftrightarrow S_{v_{1/f_b}} = \frac{\pi A_f}{\omega}$$

noise contributions

$$C_G = C_{ox} \frac{WL}{L_{ox}}$$



Introduction (unm.edu)

1/203 common source  
Transfer Function

$$f_T = g_m R_i \cdot \frac{1}{2\pi R_i C_G}$$

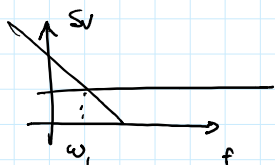
$$\omega_T = g_m / C_G$$

gate capacitance

$$g_m = \omega_T C_G$$

↑ cutoff frequency

The transistor noise is described as



to find noise corner frequency

$$\frac{\pi A_f}{\omega_c} = \frac{2kT\alpha}{g_m}$$

$$\pi A_f = \frac{2kT\alpha}{\omega_T C_G} \omega_c$$

↑ 1/f noise      ↑ white

$$ENC^2 = (C_G + C_D)^2 \cdot \frac{2kT\alpha}{\omega_T C_G} \cdot \frac{1}{Z} A_1 + (C_G + C_D)^2 \cdot \pi A_f A_2$$

series white

series 1/f

$$+ q I_{leak} Z A_3$$

parallel white

$$ENC^2 = (C_G + C_D)^2 \cdot \frac{2kT\alpha}{\omega_T C_G} \cdot \frac{1}{Z} A_1 + (C_G + C_D)^2 \cdot \frac{2kT\alpha}{\omega_T C_G} \omega_c + q I_{leak} Z A_3$$

$$= \frac{(C_G + C_D)^2}{C_G} \cdot \frac{2kT\alpha}{\omega_T} \left( \frac{1}{Z} A_1 + \omega_c A_2 \right) + q I_{leak} Z A_3$$

assuming  $\omega_T$  constant  
bandwidth

requires fixed  
current density

to minimize the ENC of the transistor, we must minimize

$$\frac{(C_G + C_D)^2}{C_G} = C_D \left( \sqrt{\frac{C_D}{C_G}} + \sqrt{\frac{C_G}{C_D}} \right) \rightarrow \text{minimize when matching}$$

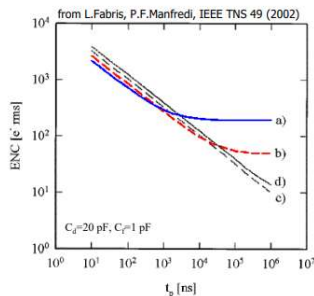
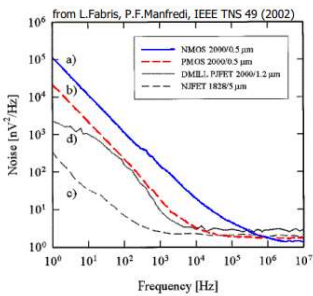
$$\frac{v_{out}}{C_G} = C_D \left( \sqrt{\frac{C_D}{C_G}} + \sqrt{\frac{C_G}{C_D}} \right) \rightarrow \text{minimize when matching}$$

$$C_G = C_D$$

matching gate capacitance with detector capacitance

Keep in mind that also gate leakage current must be considered if the transistor type is JFET or maybe BJT

$$\text{shot contribution} = q(I_{DC} + I_{gate} + \dots) Z_{detector}$$



MOSFET:

- higher g<sub>m</sub>
- smaller white noise

PMOS

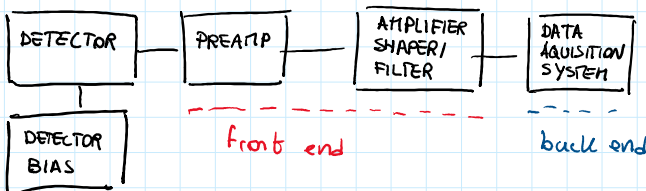
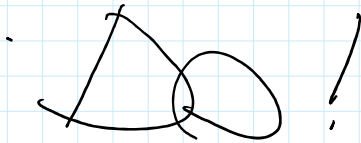
- lower 1/f noise than NMOS

if shaping times ~ 1 μs

PMOS > NMOS, JFET

### PREAMPLIFIER TRANSISTOR NOISE CONTRIBUTION

capacitor matching for save power dissipation ?



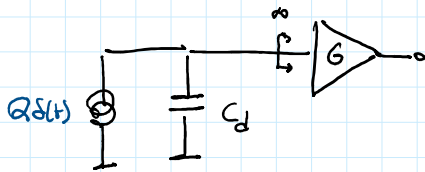
PREAMPLIFIER CIRCUIT

- why the preamplifier

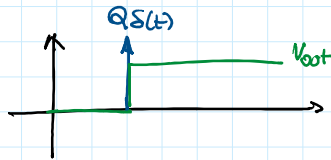
- minimize the noise of the following stages via amplification
- minimize noise contribution of transmission by placing it as close as possible to the detector

- minimize noise contribution of transmission by placing it as close as possible to the detector
- transmission signal driving

### VOLTAGE PREAMPLIFIER



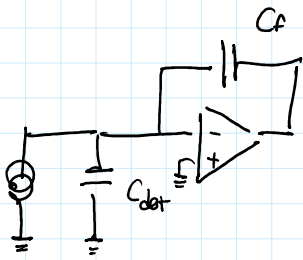
$$V_{out} = \frac{Q}{C_{det} + C_{par}} G$$



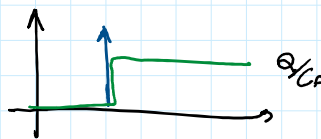
the major drawback of the voltage preamplifier is the direct dependence on

- C detector: not stable and varying with bias
- C parasitic: not stable and hardly predictable

### VOLTAGE PREAMPLIFIER



$$V_{out}|_d = \frac{Q}{C_f}$$



the charge amplifier (invented by Emilio Gatti!) solves the issue of dependence on unstable capacitance

the feedback capacitance can be freely designed and produced

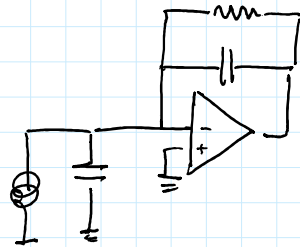
### CHARGE AMPLIFIER (IDEAL)

$$V_{out}|_{real} = V_{out}|_d \cdot \frac{1}{1 - \frac{1}{G_{loop}}} = \frac{Q}{C_f} \cdot \frac{1}{1 + \frac{1}{A \cdot \left( \frac{1}{C_{in}} + \frac{1}{C_f} \right)}}$$

$$= \frac{Q}{C_f} \cdot \frac{1}{1 + \frac{1}{A} \cdot \frac{C_f + C_{in}}{C_f}} = \frac{Q}{C_f} \cdot \frac{1}{1 + \frac{1}{A} \left( 1 + \frac{C_{det} + C_{par}}{C_f} \right)}$$

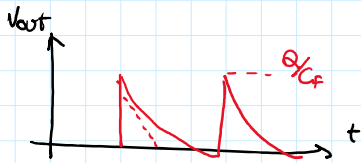
$$\text{if } A \gg 1 \Rightarrow V_{out}|_{real} \cong V_{out}|_d = \frac{Q}{C_f}$$

### FINITE GAIN FEEDBACK



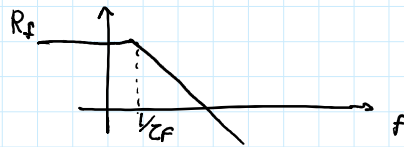
capacitor (integrator) must be reset between pulses, otherwise multiple pulses overlap

can do it with resistance or (open mosfet in short channel technologies)  
 → continuous reset

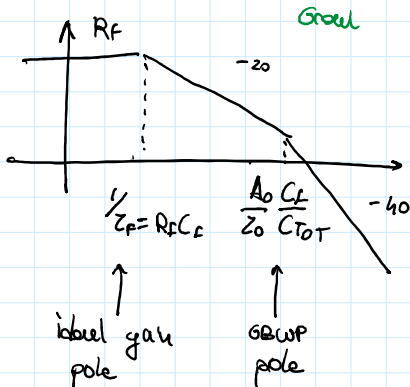


$$z = R_F C_F$$

typically  $z \approx 40 \mu s$



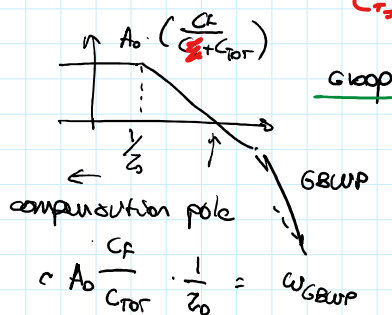
We also must consider that the amplifier often has at least one pole



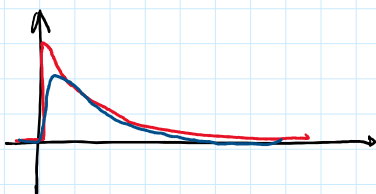
$$A(s) = A_0 \cdot \frac{1}{2 + s z_0}$$

$$z_0 = r_0 C_0$$

$$C_{TOT} = C_0 + C_F$$



### REAL TRANSFER FUNCTION



ideal  $\delta$  response      2 pole delta response

(finite rise time  $\approx 100 ns$   $\Rightarrow$  GBWP)

(Also think of this time domain response as

STEP response of a LPF + HPF  $\rightarrow$

$\delta$  is derivative of step so

STEP is integral of  $\delta$

$\Rightarrow$  same time response

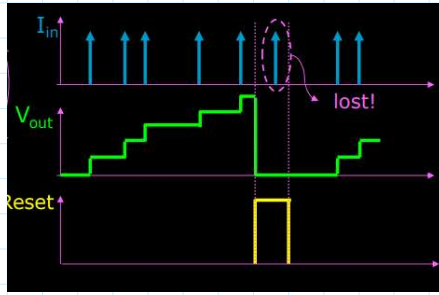
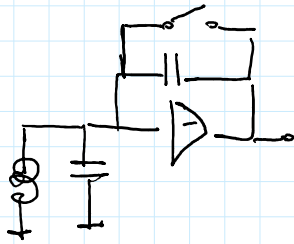
derivative  $(\cdot s)$

integral  $(\cdot \frac{1}{s})$

REAL OPAMP TIME RESPONSE

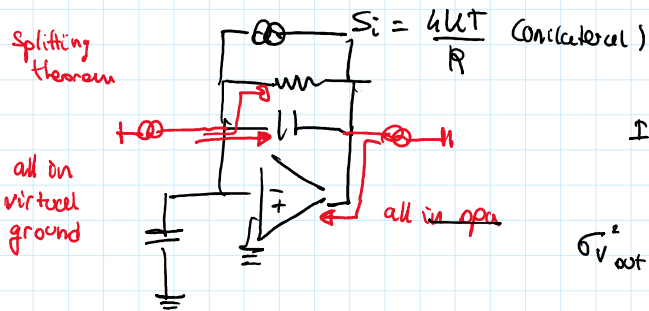
The reset can also be pulsed / non continuous

-> the capacitance can be discharged with a MOS



GATED RESET

The introduction of the reset resistor also adds a reset noise contribution



If virtual ground ideal

$$\sigma_{V_{out}}^2 = \int_0^{+\infty} S_V(f) \cdot \left| \frac{R_f}{1+sR_fC_f} \right|^2 df$$

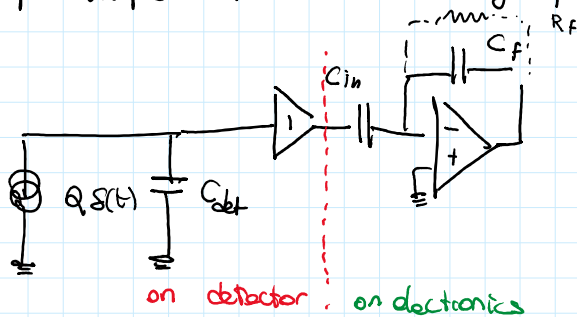
$$= \frac{4kT}{R_A} \cdot R_f^2 \cdot \underbrace{\frac{\pi}{2} \cdot \frac{1}{2\pi R_f C_f}}_{\text{Noise Eq BW}}$$

$$= \frac{kT}{C_f}$$

$$\sigma_{ch}^2 = \text{ENC}^2 = \frac{kT}{C_f} \cdot C_f^2 = kT C_f$$

kTC ON RESET RESISTOR

Example implementation of voltage preamplifier

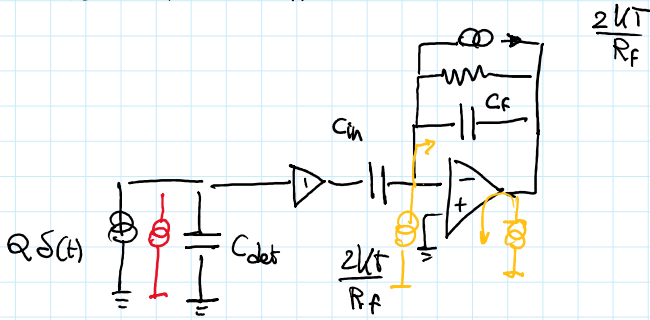


$$V_{out} = \frac{Q}{C_{det}} \cdot \frac{C_{in}}{C_f}$$

assuming ideal

↳  $\left( \frac{Z_2}{Z_1} \right)$  inverting opa

noise contribution



input referring the noise

$$F_o = F_{in} \cdot \frac{1}{\sqrt{C_{det}}} \cdot \frac{1}{\sqrt{C_{in}}} = F_{in} \cdot \frac{C_{in}}{C_{det}}$$

noise

$$* S_{in} = \frac{2kT}{R_f} \cdot \left( \frac{C_{det}}{C_{in}} \right)^2$$

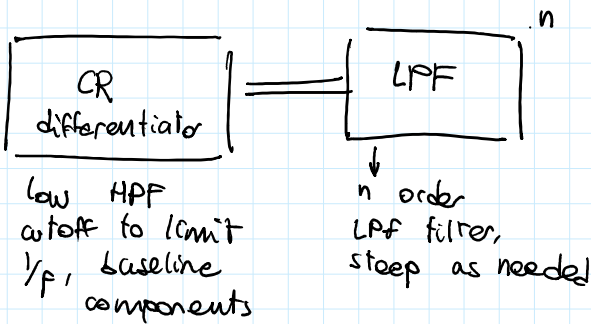
VOLTAGE PREAMP EXAMPLE

SHAPING AMPLIFIER

from the preamp, the signal comes out as a step: as the delta-pulse signal is integrated (on the detector capacitance or by a charge amplifier)

The shaper transform this step-like function in a readable pulse / signal (averaging filters to improve SNR)

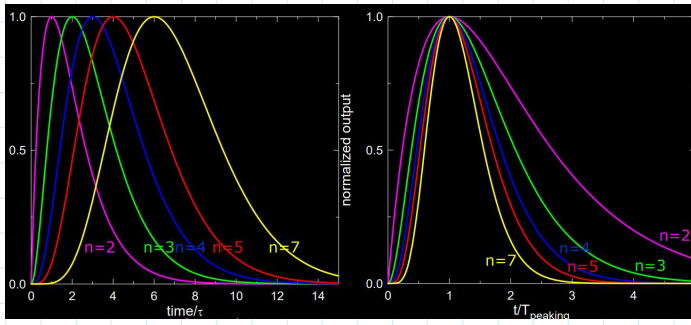
Typically



$$T(s) = \frac{s^2}{(1+s\tau)^n} \rightarrow \text{real coincident poles}$$

other LPF transfer functions can be obtained using non real poles

(chebichev - resonant filter - bessel etc)



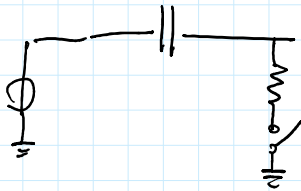
A high pass alternative that is a NON CONSTANT PARAMETER SIGNAL

is the baseline restorer

⇒ with low frequency noise:

- 1/p
- baseline offset

← needs a sync signal when signal incoming

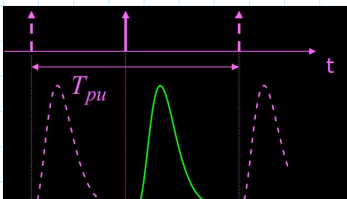


SW closed when NO SIGNAL  
↳ HPF on baseline

SW open when SIGNAL  
↳ no action on signal

SHAPING AMP TYPES/FILTERS

The shaping amplifier also has a great impact on multiple pulse detection

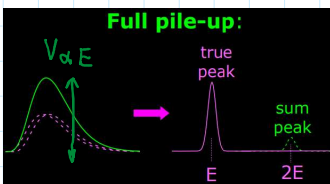


$$P_{no\ pileup} = e^{-h_n T_{pu}}$$

$$P_{pileup} = 1 - P_{no\ pileup}$$

The pileups can be of two types:

Full Pileup



Two pulses almost perfectly align giving rise to

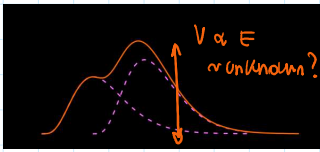
Detection of energy  $E_{pu} = E_1 + E_2 = (2E)$   
in this case

They can be recognized for 2 reasons:

- A known frequency (a combination of sum of true frequencies)
- The intensity will be with a higher counting rate

- A known frequency (a combination of sum of true frequencies)
- The intensity higher with a higher counting rate (as pileups become more probable)

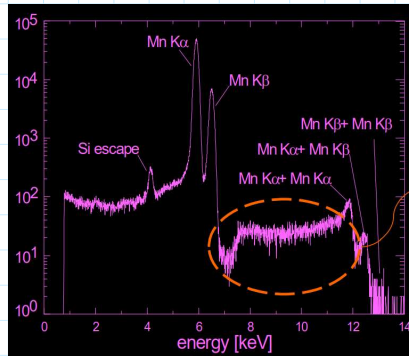
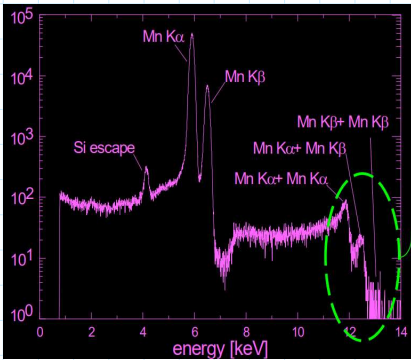
### INTERMEDIATE PILEUP



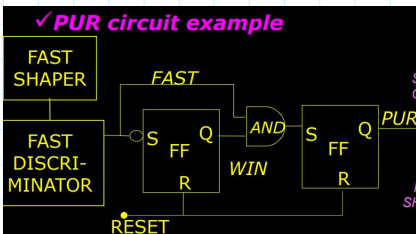
Most undesirable because:

- unknown result energy (sum can be off phase by an unknown amount)

⇒ any amplitude between peak energy and the sum can be obtained



### PILEUP

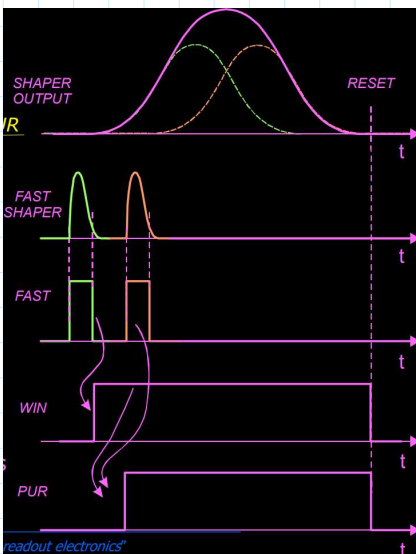


A Pile Up Rejector circuit can be used to detect pileups and take action

⇒ fast shaper allows the detection of pileups in the "normal" shaper time

↳ The fast shaper output must be compared with a threshold threshold must be chosen carefully as a fast shaper output is more noisy

- too low: false positive rejections
- too high: pileups may not trigger the PUR



### PILEUP REJECTOR

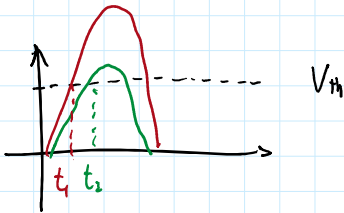
### TIME MEASUREMENTS



## TIME MEASUREMENTS

If we are interested in measuring the time of arrival of a photon, we must add a time detector circuit.

We could simply put a comparator on the shaper output. However, this is not very precise, as a comparator triggered by a fixed threshold would trigger at slightly different shape "times" depending on the amplitude (and so on the energy) of the incoming photon.



time difference on same photon arrival time

### UNIPOLAR LINE COMPARATOR

To solve the amplitude dependence issue, we can use a bipolar comparator, that triggers at the zero crossing.

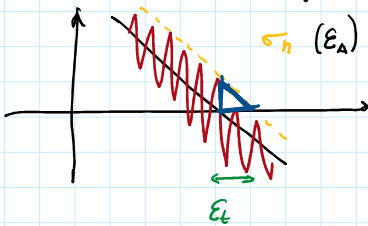


The zero crossing is not E dependent anymore

We can obtain this shape for example by differentiating the unipolar shape

### BIPOLAR COMPARATOR

However also the timing precision is affected by noise. On the zero crossing the signal affected by noise



$$E_t \cdot \left. \frac{dV}{dt} \right|_{V=0} = E_n$$

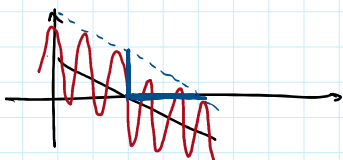
$$E_t = \frac{E_n}{\left. \frac{dV}{dt} \right|_{V=0}}$$

(linearizing at zero crossing)

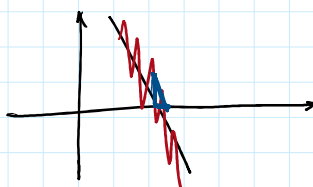
TIME JITTER RMS

We can observe that the time noise depends on the slope of the signal

However the slope of the signal is dependent on signal energy itself.



low E photons



high E photons

low E photons

low E photons

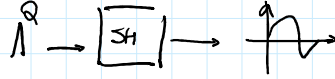
high E photons

high E photons

x with the same voltage noise, the timing precision is better on a high energy photon

We can derive an alternative expression for time jitter considering

$$G_Q = \frac{V_{max}}{Q}$$

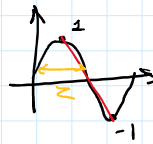


$$E_T = \frac{ENC_{in}}{\left(\frac{dV}{dt}\right) G_Q} = ENC_{in} \cdot \frac{1}{\frac{1}{G_Q} \left(\frac{dV}{dt}\right)} = \frac{ENC_{in}}{\frac{Q}{V_{max}} \cdot \frac{dV}{dt}}$$

$$= \frac{1}{SNR|_Q \cdot \frac{dV'}{dt}}$$

derivative normalized to 1

$$\frac{dV}{dt} \cdot \frac{1}{V_{max}}$$



$$\Rightarrow \frac{dV'}{dt} = \frac{2}{2Z} = \frac{1}{Z}$$

↑ two of bipolar gaussian shape

$$E_T = \frac{ENC|_Q}{Q} \cdot Z$$

# RAMO THEOREM

Saturday, July 6, 2024 10:26 AM

## SIGNAL FORMATION

The Shockley Ramo theorem states a relation between

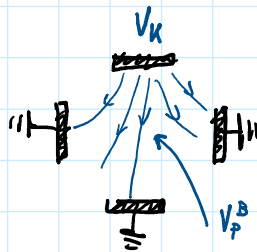
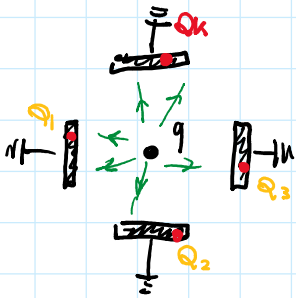
$$\text{INDUCED CURRENT ON AN ELECTRODE} \Leftrightarrow \frac{d}{dt} \text{ (RATE OF CHANGE) OF ELECTROSTATIC FLUX (E field on electrode surface)}$$



this can lead us to a relation between induced current on electrode and the instantaneous position (and movement) of moving charges

## RAMO

We start by considering the **green reciprocity theorem**. **derived from Poisson equation**. Considering two cases in the same environment



A

- Ground all electrodes
- Charge is placed in point P

↓  
the P charge generates an electric field, attracting charges on electrodes

on A

$q_p$	$V_p$
$Q_k$	$V_k$
$Q_i$	$V_i$
$\dots$	$\dots$

B

- ground all electrodes, except K
- No charge
- apply voltage on interest electrode



Voltage will be present on the point P

on B

0	$V_p$
$Q_k$	$V_k$
$\dots$	$\dots$

$$\begin{matrix} Q_k & V_k \\ Q_1 & V_1 \\ Q_2 & V_2 \\ Q_3 & V_3 \end{matrix}$$

$$\begin{matrix} 0 & V_p \\ Q_k & V_k \\ Q_1 & V_1 \\ Q_2 & V_2 \\ Q_3 & V_3 \end{matrix}$$

=> Green reciprocity theorem states that

$$\sum Q_i^A V_i^B = \sum Q_i^B V_i^A$$

$$\begin{pmatrix} q \\ Q_k^A \\ Q_1^A \\ Q_2^A \\ Q_3^A \end{pmatrix} \begin{pmatrix} V_p^B \\ V_k^B \\ 0 \\ 0 \\ 0 \end{pmatrix} = \begin{pmatrix} 0 \\ Q_k^B \\ Q_1^B \\ Q_2^B \\ Q_3^B \end{pmatrix} \begin{pmatrix} V_p^A \\ 0 \\ 0 \\ 0 \\ 0 \end{pmatrix}$$

Solving the equation for our situation

$$q_p V_p^B + Q_k^A V_k^B = 0$$

$$Q_k^A = -q_p \frac{V_p^B}{V_k^B} = -q_p \tilde{V}_w$$

| normalized  
induced charge particle charge potential at  
on plate k due to  $q_p$  point p induced  
| by 1V on  $V_k^B$  electrode  
potential at point p  
induced by electrode at  $V_k^B$   
(no charge)  
| potential inducing  $V_p^B$  on  
electrode

### GREEN RECIPROCALITY

To derive the induced current on electrode k we can use the definition of current

$$i_k(t) = \frac{dQ_k}{dt}$$

Following green reciprocity

$$i_k(t) = \frac{dQ_k}{dt} = - \frac{d(q_p \tilde{V}_w)}{dt} = -q_p \frac{d\tilde{V}_w}{dt} \cdot \frac{d\vec{\ell}}{dt} = -q_p \frac{d\tilde{V}_w}{d\vec{\ell}} \frac{d\vec{\ell}}{dt}$$

$$\begin{matrix} \vdots \\ \vec{E} = - \underline{dV}_w \\ \vdots \end{matrix} \quad \begin{matrix} \vdots \\ \underline{dx} = \vec{V} \\ \vdots \end{matrix}$$

$$\vec{E}_w = -\frac{dV_w}{d\vec{x}} \quad \frac{d\vec{x}}{dt} = \vec{v}$$

along line  $l$

$$i_x(t) = q_p \vec{E}_w \cdot \vec{v} \quad \text{scalar product}$$

weighting potential  
field: not the true  
 $\vec{E}$  field in the device,  
just the field induced  
by testing potential  $\pm V$   
on electrode

carrier velocity  
if we assume drift in  
the device, we see that  
the  $q_p$  charge velocity  
is dependent on the true  
electric field present  
in the device

$$\vec{v} = \mu \vec{E} \quad (\text{mobility model})$$

### RATIO INDUCED CURRENT

The induced charge can be calculated on the  
weighting potential map

$$\begin{aligned} Q_i(t) &= \int_0^t i(t) dt = \text{by RUMMO} = \int_0^t q \vec{E}_w \cdot \vec{v} dt \\ &= \int_{z_0}^z q \vec{E}_w \cdot d\vec{x} = -q [V_w(z) - V_w(z_0)] \end{aligned}$$

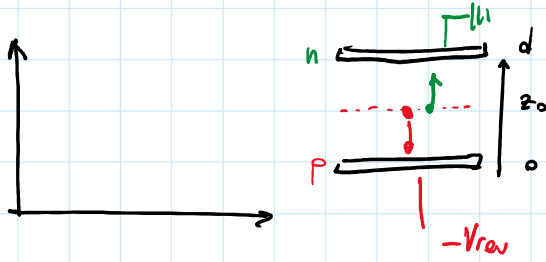
line integral

### INDUCED CHARGE

### SIGNAL FORMATION EXAMPLES

Assuming a device with  $\sim$  uniform field (like pin junction) we can derive using RUMMO the signal generated from the detector when a charge moves in it

generated from the detector when a charge moves in it



currents

$$i_n(t) = -q(-E_w \cdot v_n) = q \frac{1}{d} \cdot v_n = \frac{q}{T_n}$$

$$i_p(t) = q(-E_w \cdot v_p) = q \frac{1}{d} v_p = \frac{q}{T_p}$$

Strengthening field

$$E_w = -\frac{dV_w}{dx}$$

constant on charge

$$= \frac{V_w}{d} = \frac{1}{d}$$

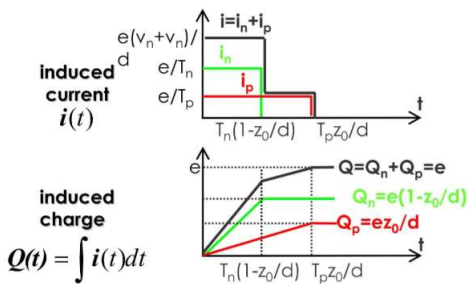
Velocity

$$v = \mu E = \mu \frac{V_{rev}}{d}$$

current depend on transit time in the whole electrode however carriers are not transiting for the whole time, as they are generated in position  $z_0$

$$\left\{ \begin{array}{l} i_n(t) \quad 0 < t < T_n \left(1 - \frac{z_0}{d}\right) \\ i_p(t) \quad 0 < t < T_p \left(\frac{z_0}{d}\right) \end{array} \right.$$

Consider that typically also,  $v_p < v_n$  as holes have a lower mobility (so lower velocity)



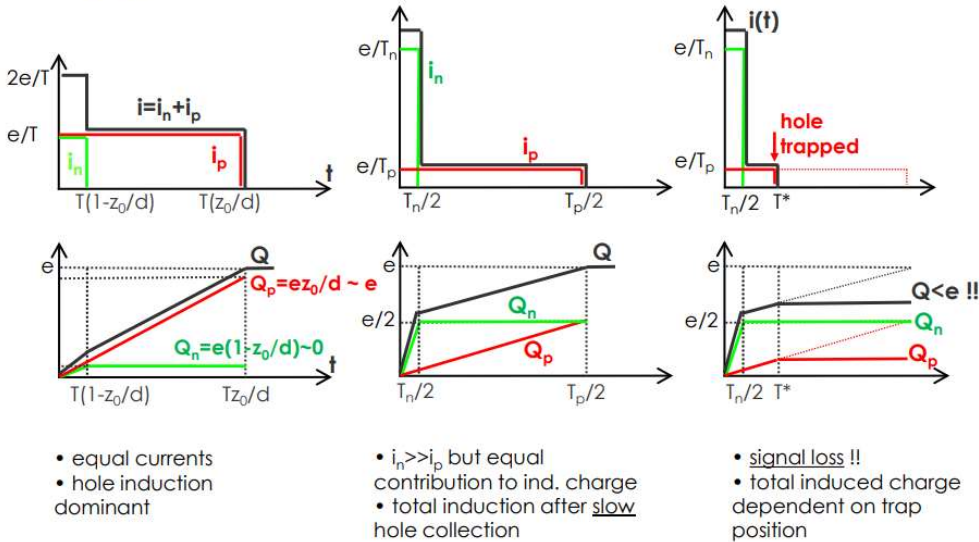
- sensing the charge moving
- not important collected charge

- at the end  $Q = q$  (collected charge)

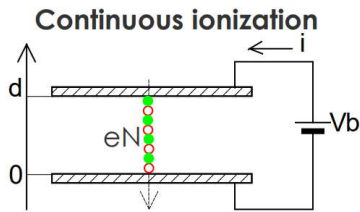
$z_0 \sim d$  (anode side)  
 $V_n = V_p = V$

$z_0 = 0.5d$   
 $V_n \gg V_p$

as before, but  
hole trapped @  $t = T^*$



✓ Induced current (charge) in planar electrode geometry

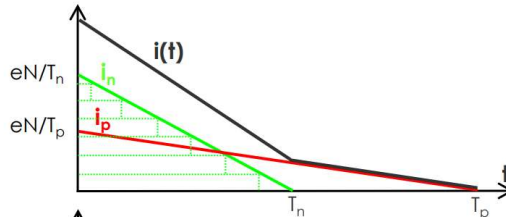


$E_w = -\frac{1}{d}k$  weighting field

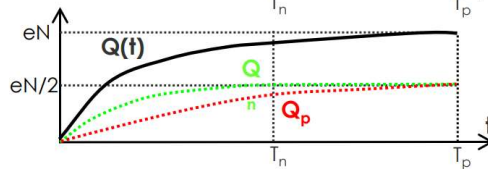
$i_n(t) = eN \frac{v_n}{d} (1 - t/T_n) = \frac{eN}{T_n} (1 - t/T_n) \quad 0 \leq t \leq T_n$

$i_p(t) = eN \frac{v_p}{d} (1 - t/T_p) = \frac{eN}{T_p} (1 - t/T_p) \quad 0 \leq t \leq T_p$

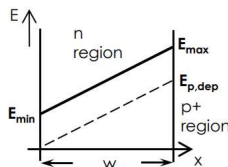
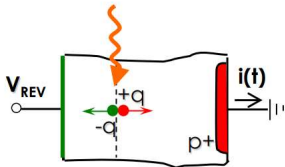
induced current



induced charge



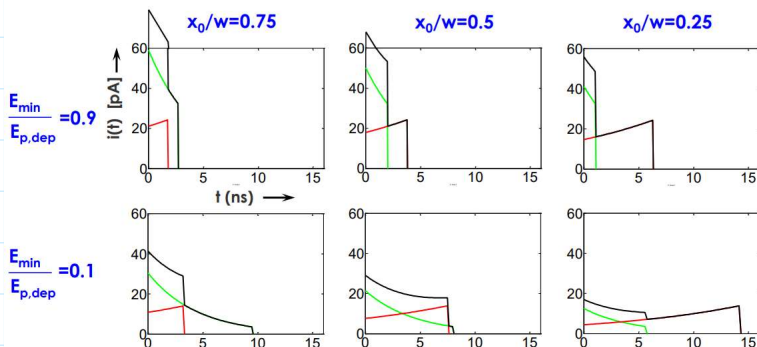
✓ Induced current (charge) in pn junction



induced currents

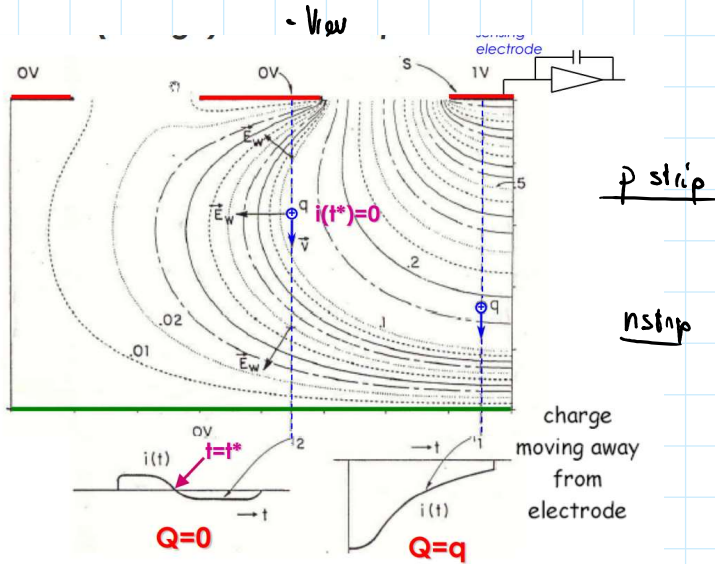
$i_h(t) = \frac{q}{w} \mu_h \left( E_{min} + \frac{qN_D}{\epsilon} x_0 \right) e^{\mu_h q \frac{N_D}{\epsilon} t} \quad 0 \leq t \leq t_h$

$i_e(t) = \frac{q}{w} \mu_e \left( E_{min} + \frac{qN_D}{\epsilon} x_0 \right) e^{-\mu_e q \frac{N_D}{\epsilon} t} \quad 0 \leq t \leq t_e$



Silicon  
 $w = 300 \mu m$   
 $N_D = 10^{12} cm^{-3}$   
 $E_{peak} @ V_{dep} = qwN_D/\epsilon = 4800 V/cm$

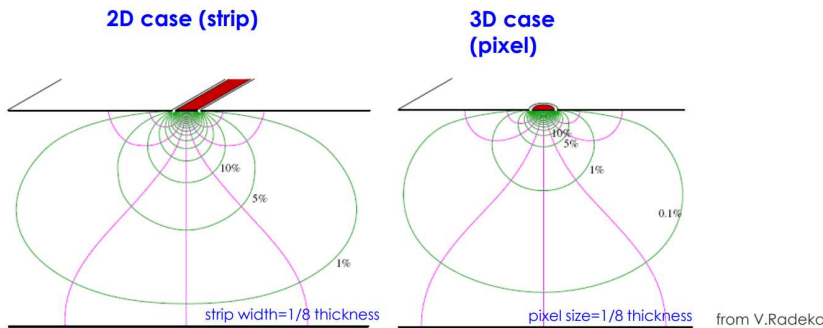
INDUCED CURRENT EXAMPLES



$i = q \vec{E}_w \cdot \vec{v}_w$       Stripe detection mechanism

here if we can observe 2 things

- a charge detected by one strings can influence the current on another electrode
- the strip width - comparable with detector depth - makes the  $E_w$  weighting field evenly distributed  
 => charge collected on detector depends on starting position of ionization



We can reduce both effects by having a strip width  $\ll$  detector depth

↳ Weighting potential will be concentrated only near the strip



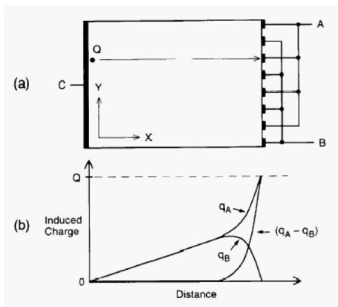
→ Weighting Potential will be concentrated only near the strip

⇒ Charge collection will be almost independent on initial ionization position:

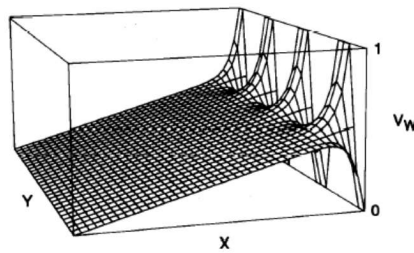
$Q \approx q$  (as if it started from one electrode to another)

⇒ also almost independent for hole trapping

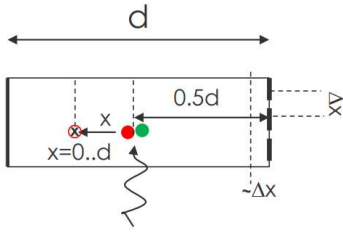
## DETECTOR STRIPS



P. Luke, IEEE Trans Nucl. Sci., vol.42, no.4, Aug. 1995



- Two inter-digitated coplanar grid electrodes sense the motion of charge carriers in the detector (solid-state equivalent of the "Fritsch grid" of gas detectors)
- A small potential difference applied btw the C-grid and NC-grid to avoid charge sharing and double polarity signals
- When generated in the bulk, a charge carrier induces equal amount of charge on the 2 grids. A net difference signal is induced only when the carriers to be collected (e.g. electrons) are close to the coplanar electrodes.
- The net result is a measured charge nearly independent of the interaction depth

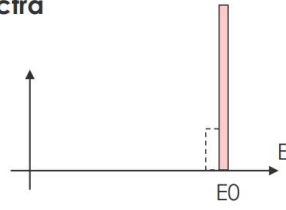


- radiation incident at  $x=0.5d$ .
- hole trapping can take place at  $x$ , uniformly distributed within  $(0, 0.5d)$
- electrons travel with no trapping

### energy spectra



single-ended readout

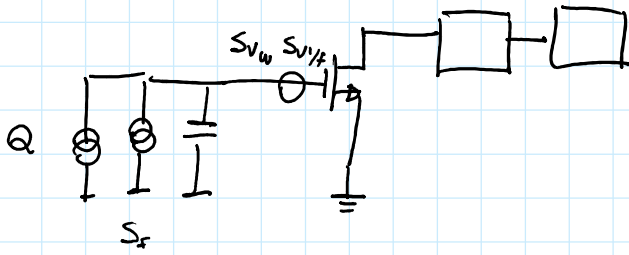


differential readout

# ADVANCED SILICON DRIFT DETECTORS

Sunday, July 7, 2024 4:53 PM

Detector capacitance plays a key role in ENC



series white

$$ENC^2 = A_1 \frac{2kT\alpha}{\omega_t} C_d \cdot \left( \sqrt{\frac{C_G}{C_D}} + \sqrt{\frac{C_D}{C_G}} \right)^2 \frac{1}{z}$$

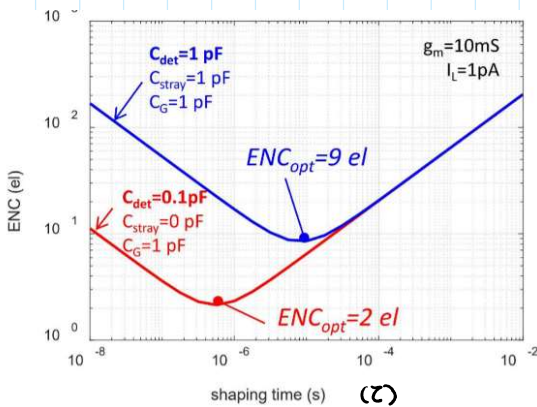
$\uparrow$   $f_{cutoff}$  MOSFET

parallel white

$$+ A_2 (\pi A_f C_G) C_d \left( \sqrt{\frac{C_G}{C_D}} + \sqrt{\frac{C_D}{C_G}} \right)^2 + A_3 (qI_L) z$$

series  $1/f$

when matching cap



Supposing we can optimize setting

$$C_{gate} = C_d$$

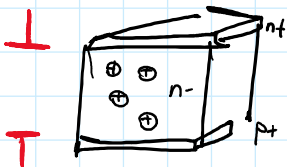
$\Rightarrow C_d$  determines contribution of series white and  $1/f$

$\Rightarrow ENC_{opt}^2$  (found by setting white par = white zero)  $(z)$

$$\propto \sqrt{\frac{C_d I_L}{\omega_t}}$$

lowering as much as possible  $C_d$  increases SNR

## DETECTION CAPACITANCE

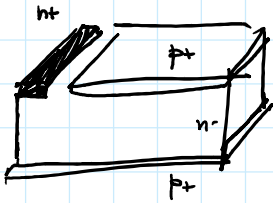


classic p-n junction

- A of capacitance is big
- thickness cannot be increased due to technology limitations

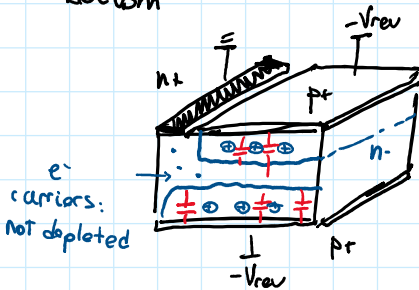
- thickness cannot be increased due to technology limitations (wafer thickness)
  - would increase active area
  - would decrease capacitance

CLASSICAL PIN LIMITATIONS

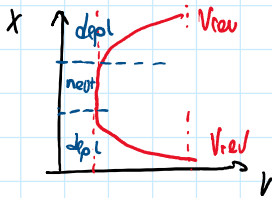


To increase active we could develop the pin horizontally

(like in classic pin junctions) both on top and bottom

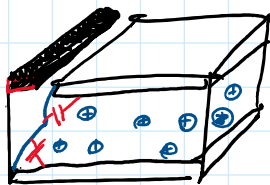


this however still results in a big depletion capacitance (big active area on both sides)



no E field in the neutral region

- ⇒ However, to increase performance, we can deplete the whole n- thickness
- ⇒ this gives a huge advantage in capacitance as in fact reduces active area of the depletion region



The active area now becomes basically only the fringing contribution

We must deplete all the device

$$V_{rev} = \frac{qN_b}{2\epsilon_0\epsilon_s} x_d^2 = \frac{qN_b}{2\epsilon_0\epsilon_s} \left(\frac{d}{2}\right)^2 = \frac{1}{4} \frac{qN_b}{2\epsilon_0\epsilon_s} d^2$$

↑  
both sides → half thickness

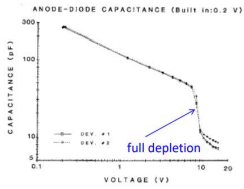
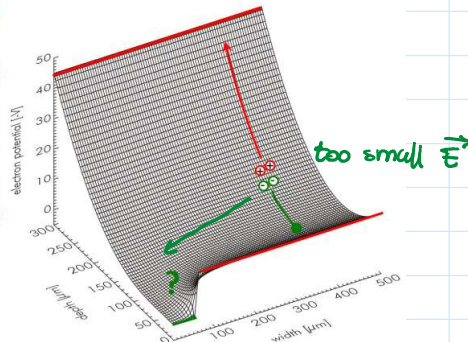
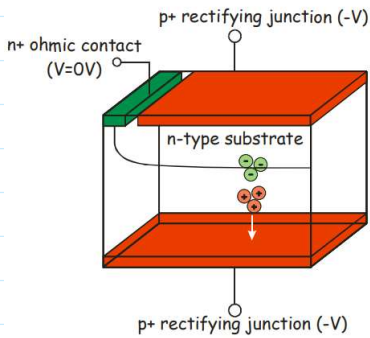


Fig. 12. Capacitance versus voltage plots of two of the test devices provided on the wafer for monitoring its doping uniformity.

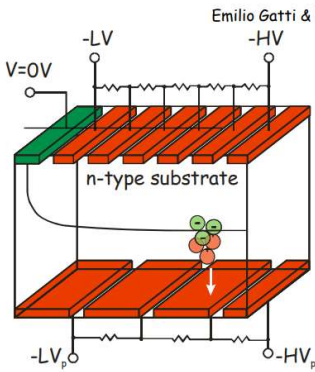
## EMILIO GATTI SILICON DRIFT DETECTOR - FULL DEPLETION

However this design present an issue .  
 The drift between the two contacts is too slow  
 as the field bends only toward the center



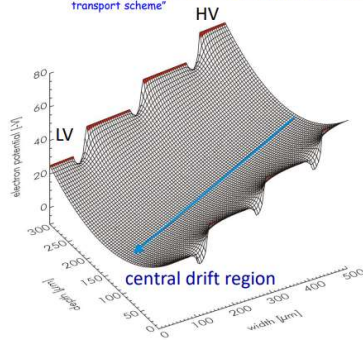
- ⊕ fully depleted volume
- ⊕ capacitance value has been decoupled from detector active area
- ⊕ holes are (quickly) collected by the p+ electrodes
- ⊖ How to bring electrons toward the anode?

## PN COLLECTION DRIFT



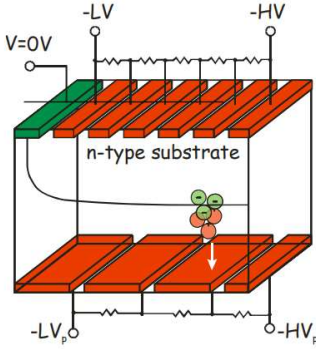
Emilio Gatti & Pavel Rehak, 1983

• Proc. of the DPF Workshop on Collider Detectors, LBL (March 1983): "The concept of a solid state drift chamber"  
 • 2nd Pisa Meeting, Grosseto, Italy (June 1983), "Semiconductor Drift Chamber - an application of a novel transport scheme"

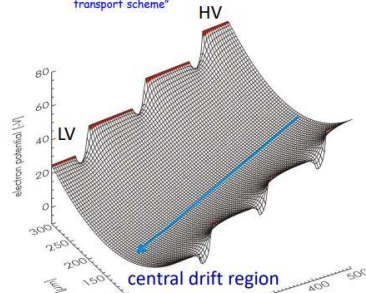


- p+ junctions segmented in strips with linearly increasing potential on both sides
  - uniform drift field parallel to the surface
  - signal electrons are focused in the center of the wafer and drift at constant velocity towards the readout anode

Emilio Gatti & Pavel Rehak, 1983

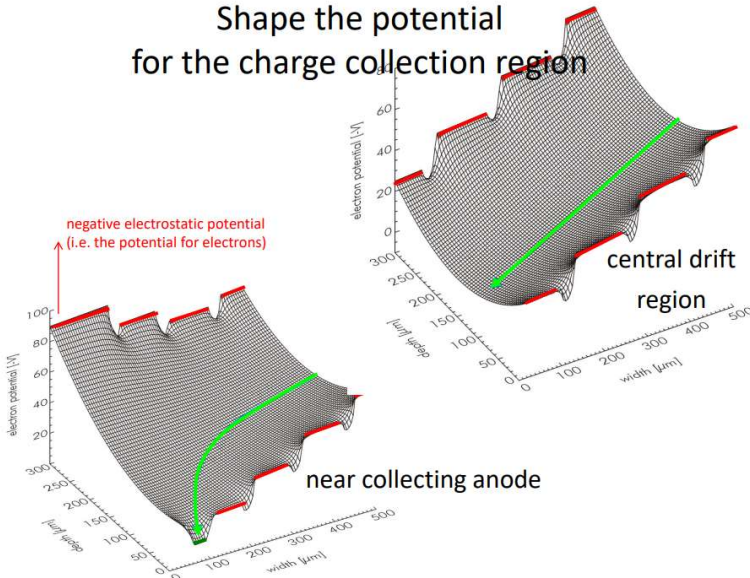


• Proc. of the DPF Workshop on Collider Detectors, LBL (March 1983): "The concept of a solid state drift chamber"  
 • 2nd Pisa Meeting, Grosseto, Italy (June 1983), "Semiconductor Drift Chamber - an application of a novel transport scheme"



- p+ junctions segmented in strips with linearly increasing potential on both sides
  - uniform drift field parallel to the surface
  - signal electrons are focused in the center of the wafer and drift at constant velocity towards the readout anode

### Shape the potential for the charge collection region



### The simultaneity of ideas

A novel avalanche photodiode (APD) concept, "the channeling APD"

P-N hetero-junction (e.g. AlGaAs/GaAs)

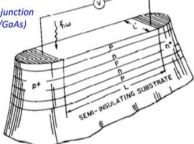


Fig. 1. Schematic diagram of the APD (not to scale). The p+ layers have a wider band gap than the n- layers. See text for layer thicknesses

- Using a new interdigitated p-n junction structure, electrons and holes are spatially separated and impact ionize in layers of different band gap.
- Thus the effective ionization-rates ratio can be made extremely high ( $\alpha/\beta > 100$ ), while maintaining a high gain,

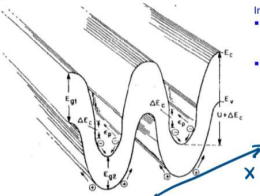


Fig. 3. Band diagram of the APD under operating conditions.  $\epsilon_p$  is the parallel electric field causing carriers to impact ionize.  $U$  is the sum of the punchthrough voltage and the built-in potential. The valence-band discontinuity has been assumed negligible with respect to  $\Delta E_c$ . Shown is also the electron-hole separation mechanism.

- Important features:
- the unique capacitance-voltage characteristic
  - the interdigitated geometry which allows the depletion of large volumes of semiconductor materials

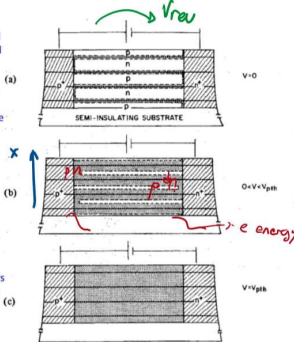


Fig. 2. (a) Cross section of the interdigitated p-n junction device at different reverse-bias voltages  $V$ . (b) The shaded portions of the layers represent the depleted volume which increases with increasing bias. (c)  $V_{p2}$  is the voltage at which the layers are completely depleted. Any further increase in bias adds a constant field  $\epsilon_p$  parallel to the device length. Note that to ensure a parallel electric field constant along most of the device length  $L$ , this dimension is made much greater than the layer thicknesses. For simplicity of illustration equal doping levels ( $p = n$ ) have been assumed.

F. Capasso, IEEE Trans. Electron Device, Vol. ED-29, No.9, Sept. 1982

## SILICON DRIFT DETECTOR

### SPECIALIZED SDD

- SPECTROSCOPY ORIENTED

these SDD do not implement position sensing,

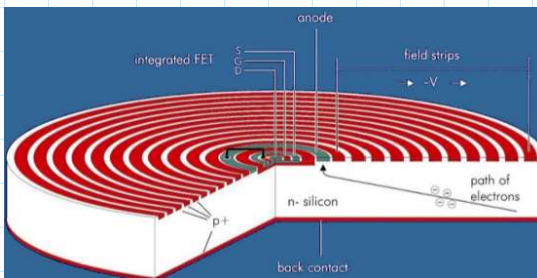
• SPECTROSCOPY ORIENTED

these SDDs do not implement position sensing, they use all their active area to collect photons and distinguish between different impacting photon energies

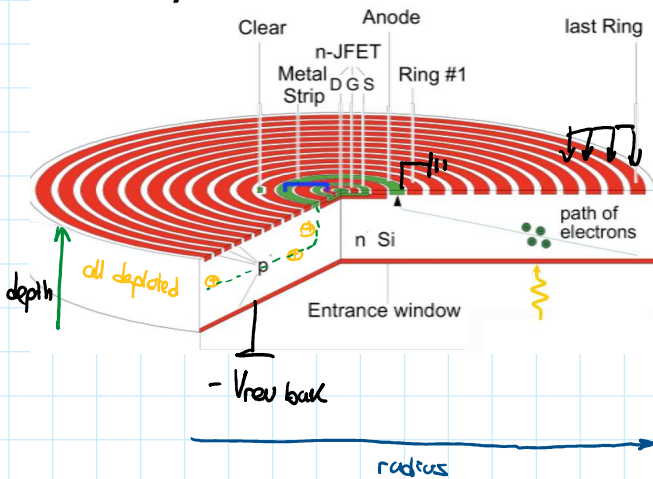
• POSITION SENSING

These devices detect the impact point like a camera constructing an image (20-30-16 too) they can or cannot distinguish different energies of impact photons (pixel spectroscopy)

SPECTROSCOPY

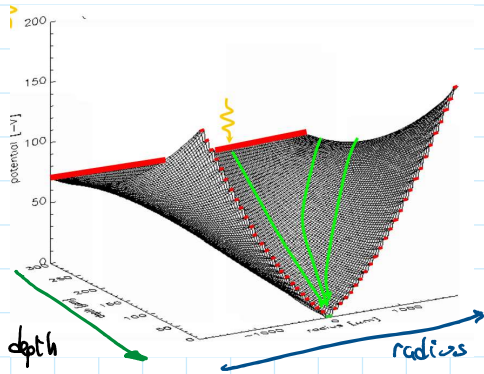


- Cylindrical shape: single anode in the center  
↳ all active area to detection
- Back contact is the exposed side to photons  
↳ p+ back contact is an entrance window
- Transistor is integrated in the detector, directly connected to the anode



p+ front segmentation: makes the electrons drift toward the center

p+ back p+ front: serves to fully deplete the SDD (-cap) and divide the carriers toward the center depth



$A_{\text{typical}} = 5-100 \text{ mm}^2$   
 $\text{Dot cap} = 100-200 \text{ fF}$

### CYLINDRICAL SDD

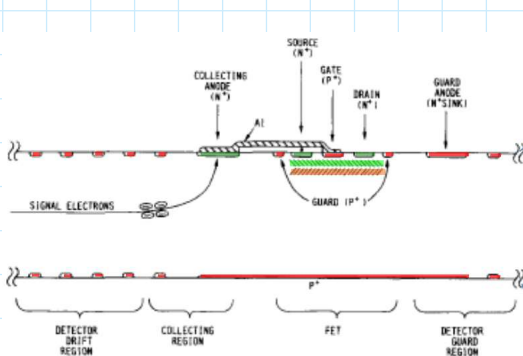
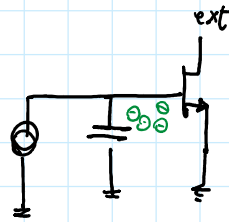


Fig. 1. Qualitative cross section of a completely depleted detector (semiconductor drift detector) with the input FET integrated on it.



The p+ guards protect the electron from being collected in the source of the JFET (at ground too) or drain

=> electrons are collected in the anode, connected at  $V_{D0}$  to the gate of the JFET via a **short metal contact**

- ↳ extremely low capacitance on precup
- ↳ smaller contributions on series noises (dependant on capacitance)

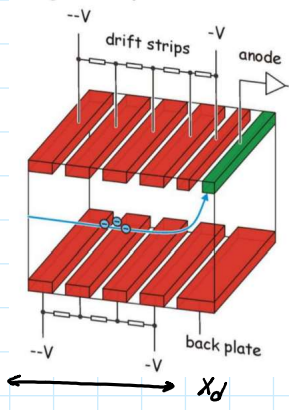
The cylindrical shape enables a  $360^\circ$  collection

### POSITION SENSING SDD

Position sensing can be achieved

- via subdivision of strips in anode
- via timing correlation





Assuming

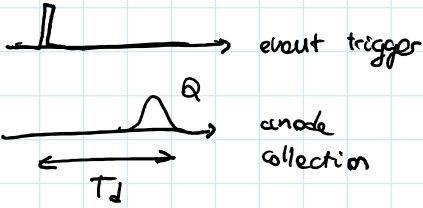
• constant Electric field  
(in low doped n-  $\frac{dE}{dx} \sim 0$ )

=> constant drift velocity

$$v = \mu E$$

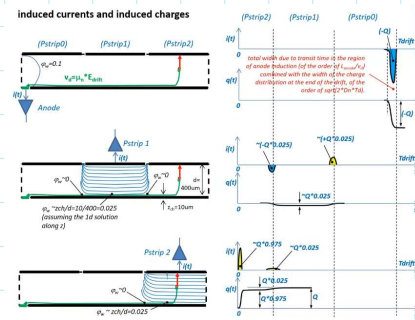
(or due to sizing of  $V_{saturation}$  in whole device)

$$X_d = v_{drift} \cdot T_d$$

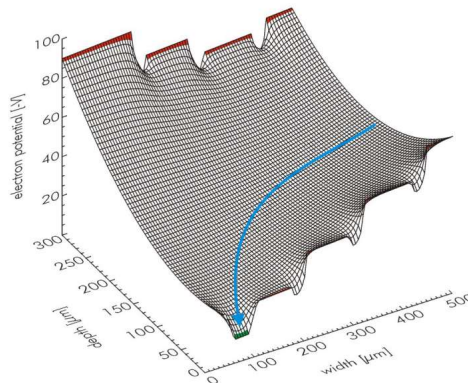
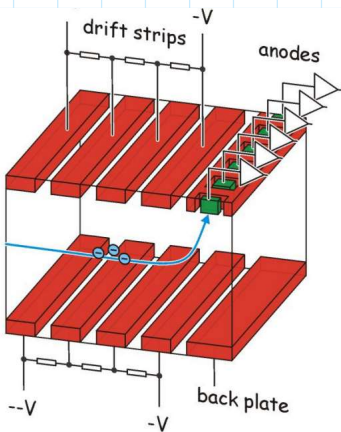


Even if the relation is not so linear, it can be derived from testing)

Need of a start event trigger  
 x available if hit time is known - a priori  
 (particle accelerators)  
 x can be derived by holes generation;  
 immediately collected by pt strips



### 2D POSITION SENSING W TIMING

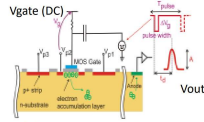


2D position sensing can be achieved combining  
 multiple strips on one direction and  
 timing detection on the other

### 2D DETECTION

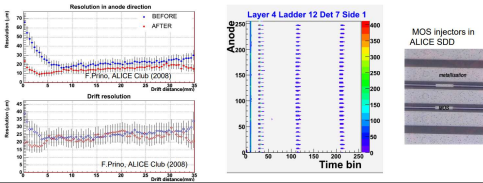
**MOS injectors**

Drift speed depends on T:  
 $V_{drift} = \mu_n E = \mu_n(T) \times T^{-2.4}$   
 Charge injectors allow real-time calibration of time vs. distance curve



calibration mechanisms for timing detection.

**Position resolution after calibration ~20-25 μm**

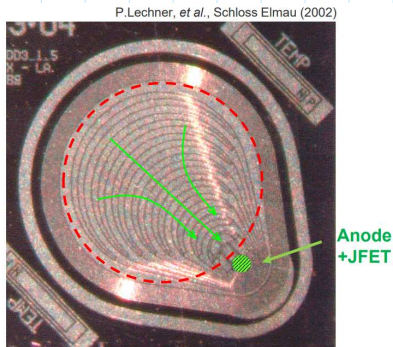
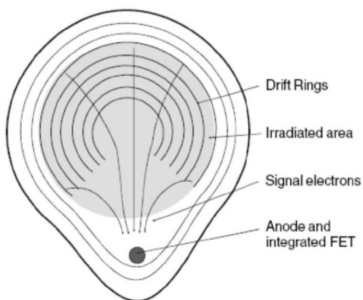
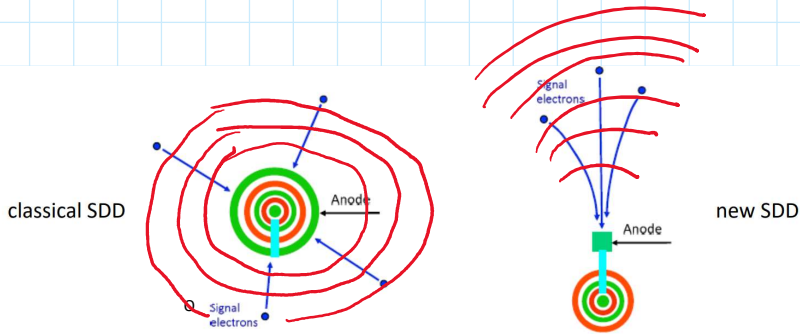


**ADVANCED SDD CONCEPTS**

The cylindrical SDD needs a circular anode  
 => big capacitance

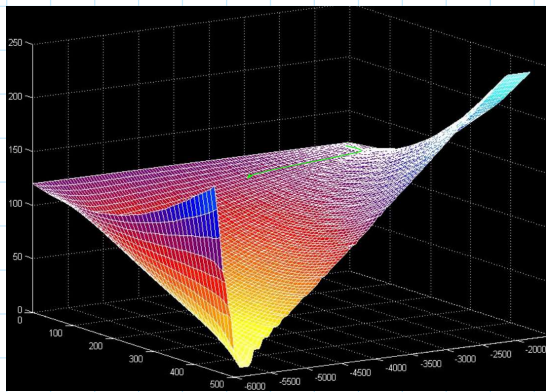
If we instead of making the device circular, but we instead make it strip-like

**drift strips**



P. Lechner, et al., Schloss Elmau (2002)

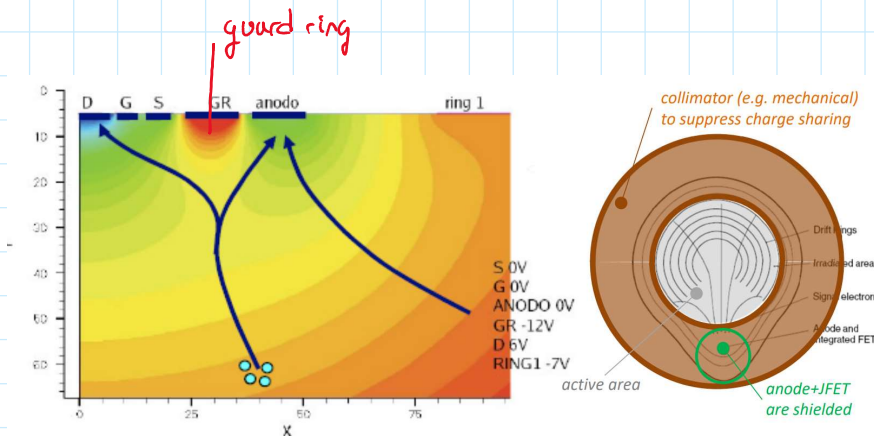
This can optimize the area of the anode and the SFET making it less and optimizing for ENC.



SDD vs SDD3 comparison

	Circular	Droplet
Anode capacitance (independent from the area)	$C_D + C_G = 230\text{fF}$	$C_D + C_G = 120\text{fF}$
Energy resolution (@ Mn-K $\alpha$ , 20°C Peltier-cooled)	$DE_{FWHM} = 130\text{eV}$	$DE_{FWHM} = 123\text{eV}$
Opt. shaping time	1 $\mu\text{s}$	0.5 $\mu\text{s}$
Peak to valley ratio	$< 10^4$	$\sim 2 \cdot 10^4$
On-chip electronics	yes	yes

DROPLET SDD SDD



If a interaction (photon hit) happens near the anode region, it may happen that the interaction charge is splitted above the guard ring and some can pass directly to the drain

=> this can be avoided putting a screen on the outer ring, avoiding collisions near the anode

↳ beware that the anode region will not be active area.

GUARD RING - NEAR ANODE COLLECTION

\* Silicon Escape spectroscopy  
- important to signal formation and radiation loss.

When doing spectroscopy, we are correlating the charge sensed via the electronics created by

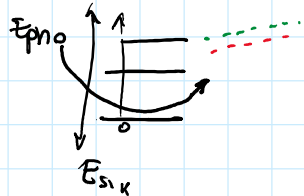
When doing spectroscopy, we are correlating the charge sensed via the electronics created by the incoming photon via the mean creation energy

$$\langle N \rangle = \langle w \rangle E_{ph}$$

However, it can happen that incoming high energy photons interact with the lower shells of silicon



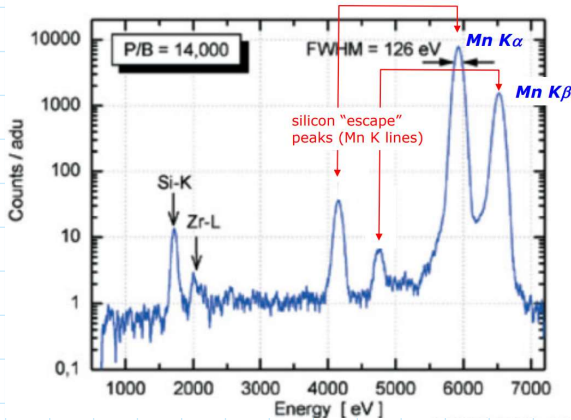
$$\langle N_D \rangle = \langle w \rangle E_{ph}$$



$$\langle N_D \rangle = \langle w \rangle \cdot (E_{ph} - E_{si,k})$$

This results in "false" incoming detected photons at energies at exactly

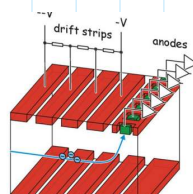
$$E_{ph} - E_{k,si}$$



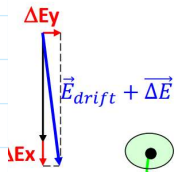
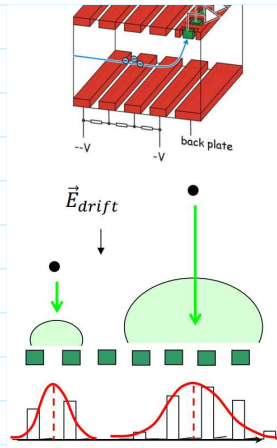
SILICON ESCAPE PEAKS

2D position sensing in MULTI ANODE SDD presents some limitation

- x timing direction  
=> a trigger is needed
- x strips direction



> strips direction  
 => drift adds a non equal distribution of the charge cloud depending on the timing direction position  
 => drift direction is also not perfectly vertical due to systematic process errors

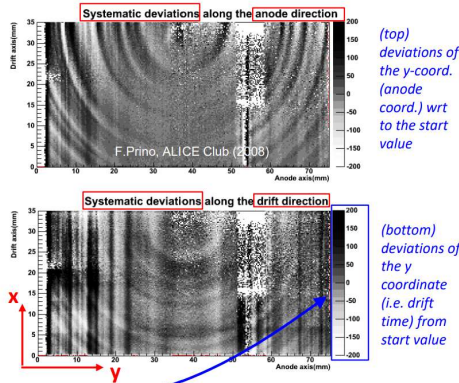


random electric field ( $\Delta E_x, \Delta E_y$ ) due to doping inhomogeneities  $\rho = \rho_0 + \Delta\rho(x, y, z)$  is superposed to the designed drift field (expected from bias voltages and uniform  $\rho_0$ ) and affects electron trajectory



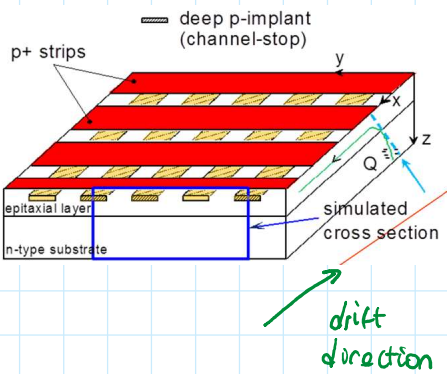
⊗ systematic deviations (up to  $\pm 200\mu\text{m}$ ) on both  $x, y$  due to doping inhomogeneities

Laser mapping measurements



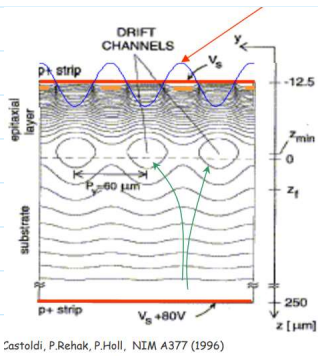
LIMITATIONS OF MULTI ANODE STRIP DET.

Suppression of lateral broadening with channel-stops

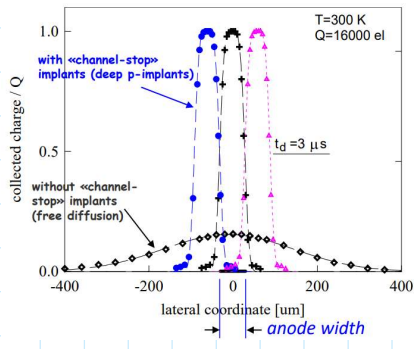


The p channel implant are put at a higher (negative, in module) potential to reject electron

In this way electrons drift in this channels avoiding dispersion in a charge cloud

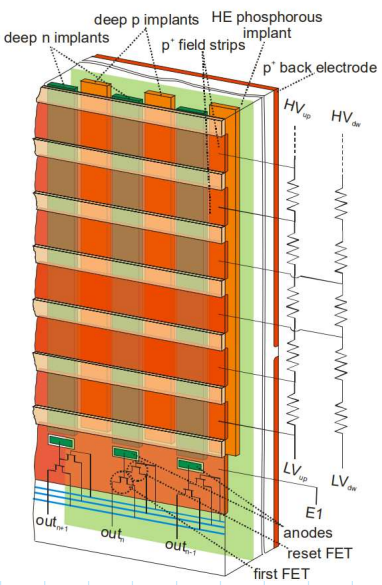


Castoldi, P.Rehak, P.Holl, NIM A377 (1996)



experimental results

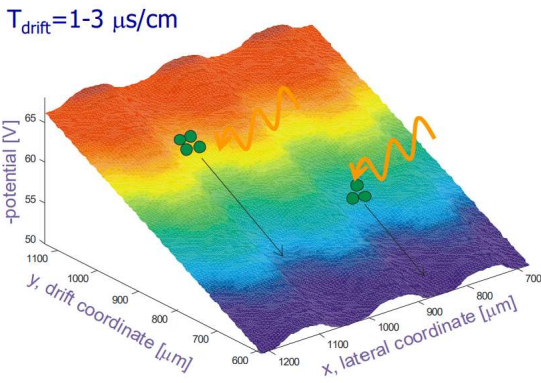
These devices are called Multi Linear SSD



- fully depleted n-type bulk
- p+ entrance window implanted on the back side
- array of p+ strips implanted on the front side
- channel-stops (deep p-implants) for lateral confinement
- channel-guides (deep n-implants) for lateral confinement and drift enhancement
- HE n implant locates the drift channel close to the finely structured surface
- on-chip electronics (JFET in source follower configuration)

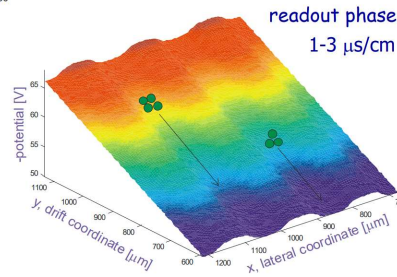
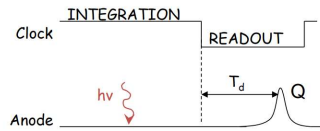
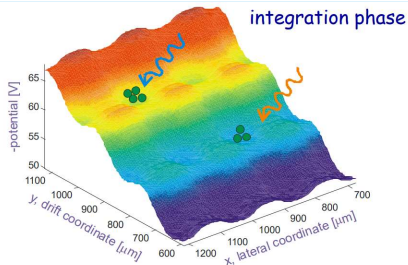
more deep so asymmetric potential on depth  
 => n+ implants  
 => drift line (minimum energy line) closer to surface

As before, trigger must  
 y external  
 x from holes to p+ immediate collection on strips/



MLSDD MULTI LAYER SSD

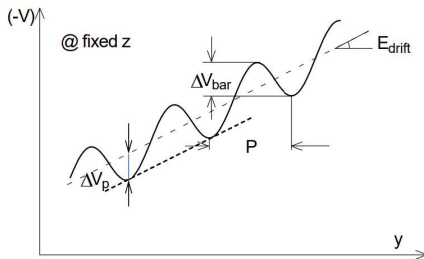
The MLSDD can also be used in another mode, as CDD (Controlled Drift Detector)



integrate-readout mode  
Controlled-Drift Detector (1997)

IEEE TRANSACTIONS ON NUCLEAR SCIENCE (1997)  
Conception and Design Criteria of a Novel Silicon Device for the  
Measurement of Position and Energy of X Rays

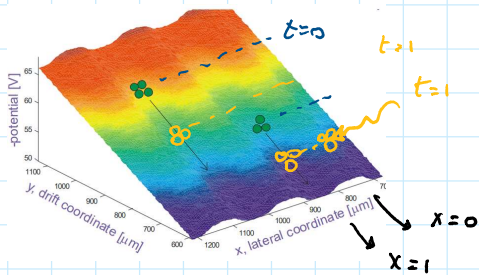
The voltage on the drift strips can be alternated during an "integration time" in which potential wells are created and electrons accumulate in it. When the voltage is then set to drift mode, all the integrated charge is collected via the classic readout.



In the CDD however there is one problem

"out of time events"

- \* If during readout a photon collection happens it may seem from a different position than when it happened



photon hit on  $x=0$   
happened on  $t=1$  of  
drift time  
 $\Rightarrow$  it is detected  
as if it was collected  
at depth equal of  
the collection on  $x=1$

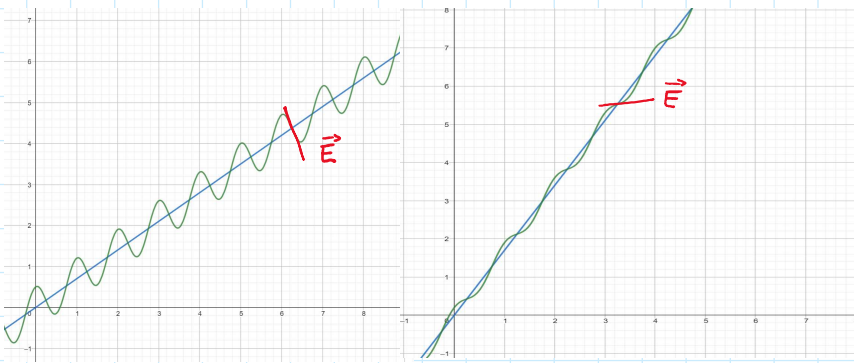
To solve this problem we should stop the detection during readout time (maybe with a mechanical mask, not so easy)

CDS CONTINUOUS DRIFT DETECTOR

$$V = E x_{drift}$$

$$V_{pot} = V_{drift} + V_p \cos\left(2\pi \frac{x}{P}\right)$$

$\hookrightarrow$  pitch



$$E = -\frac{dV}{dx} = 0$$

$$\frac{dV_{drift}}{dx} + V_p \frac{2\pi}{P} \sin\left(2\pi \frac{x}{P}\right) = 0$$

$\downarrow$  max point

$$E_{drift} + V_p \frac{2\pi}{P} (1) = 0$$

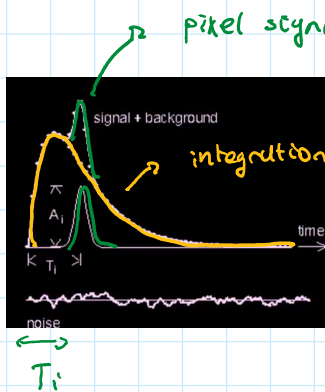
$$V_p = \frac{E \cdot P}{2\pi}$$

$$\text{if } V_p > \frac{EP}{2\pi} \Rightarrow \text{field inversion}$$

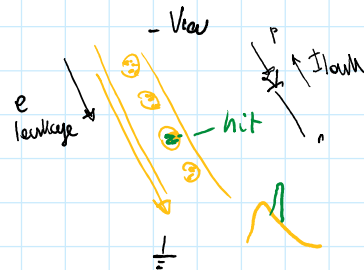


SIZING OF AV BARRIER

The integration - drift device has however a readout slightly different

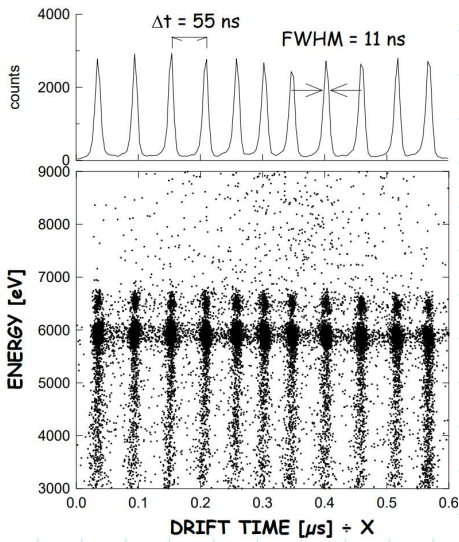


of only background change in all x wells



BACKGROUND PEAK





correlating every reading with

- time  
=> x (drift) position
- amplitude  
=> collected charge (energy)

↓ we can obtain a 2D spectroscopy image!

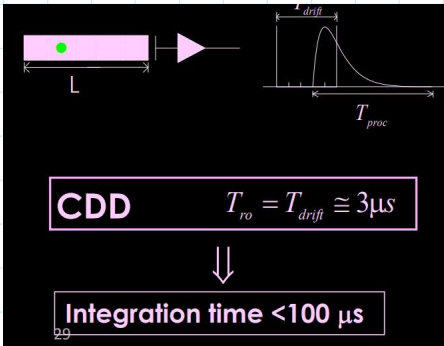
(attention: in the integration time more photon hits could pile up!

=> may lead to "fake" high energies detected)

### IMAGING SPECTROSCOPY

CDD are really fast as they do the whole readout of the image in one shot

$$\text{CDD time} \sim T_{\text{drift worst}} + T_{\text{shaper}}$$



Extremely fast respect to CCD detector! (however works best with low level of occupancy)

CCD

CHARGE COUPLED DEVICES

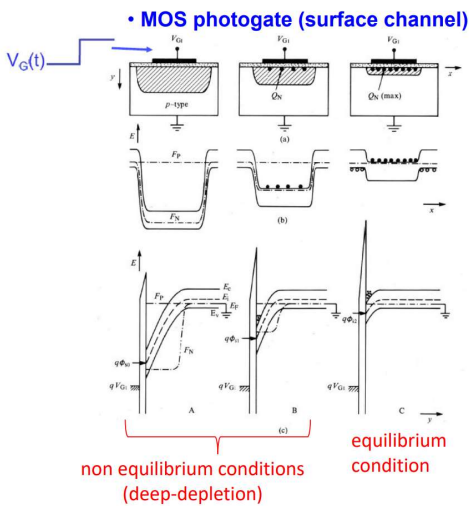
A limitation of CCD is that the readout of the image happens pixel by pixel

$$CCD_{time} = N_{pix} \cdot \left( \frac{1}{f_{shift}} + T_{processing} \right)$$

- $\frac{1}{f_{shift}}$  is basically the shift time from 1 pixel to the next
- $T_{processing}$  is the  $\tau$  of the output shape (shaper) optimized to reduce ENC  
 => if integration time too large we increase leakage current  
 If we want higher frequencies, (readout time less) shot noise we have to cut down processing time, increasing the noise  
 ↳ or cool the device wildly!

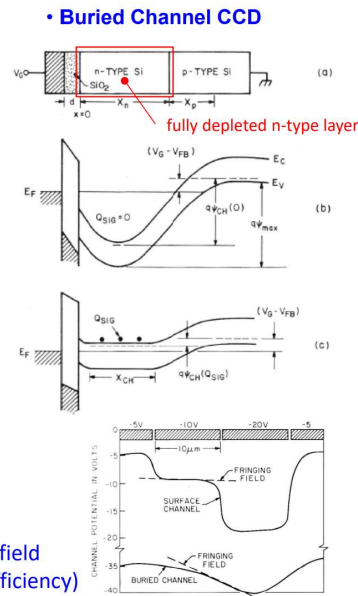
higher frequency  
higher resolution

CCD TIMING IS SLOW RESPECT TO CCD



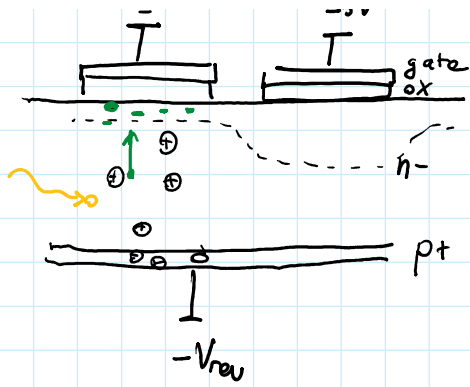
non equilibrium conditions (deep-depletion)      equilibrium condition

Impact on the fringing field (and charge-transfer efficiency)



BURIED MOS

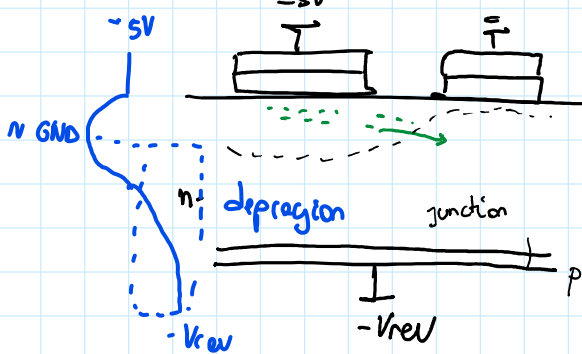




full depletion

the collected charge accumulates under the "active" gate

the transfer happens moving the potential on the gates



CTE  
charge transfer efficiency

(electrons move towards higher potential)

- also drift in buried MOS happens  $n$  at the center depth of the channel
- not near the surface



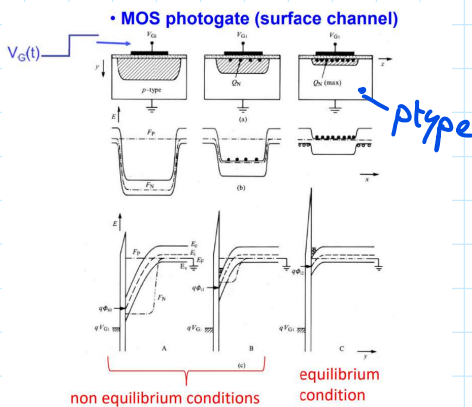
### MOS PHOTOGATE

We could replace the pn fully depleted junction directly with the MOS capacitor

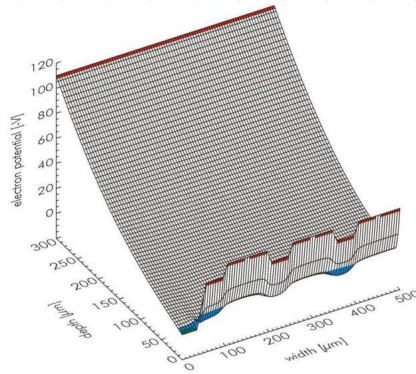
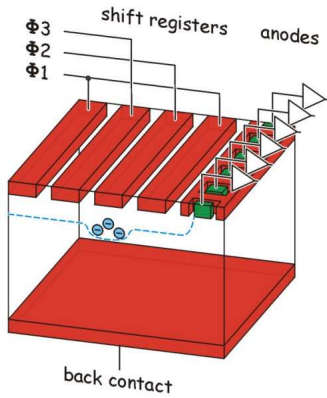
We can create a <sup>deep</sup> depletion region using the MOS field effect and collect the generated charge in the depletion region in the channel

then transfer it using the gate potentials like before

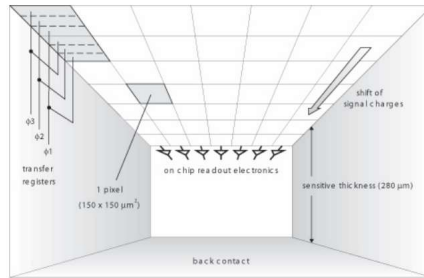
the transfer will happen near the MOS surface (new  $\text{SiO}_2$ )  
 => not good because of tripping



MOS CCD TYPES



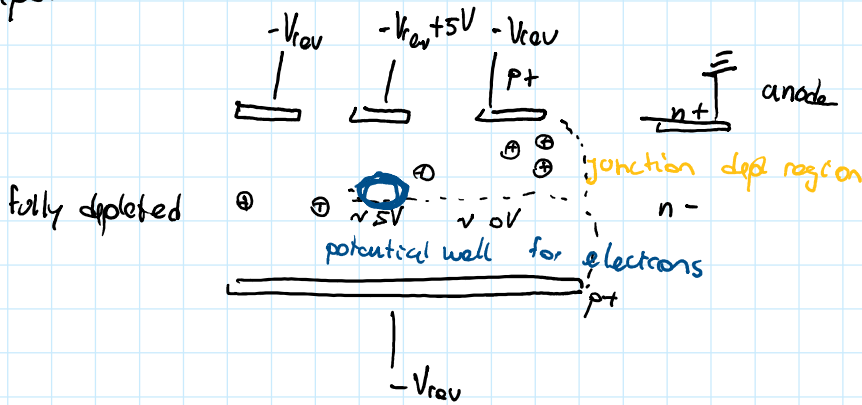
- full depletion (50  $\mu\text{m}$  to 500  $\mu\text{m}$ )
- back side illumination
- radiation hardness
- high readout speed
- pixel sizes from 30  $\mu\text{m}$  to 1 mm
- charge handling: more than  $10^6$  e<sup>-</sup>/pixel
- high quantum efficiency



Another similar implementation of CCD can be done without a MOS capacitor.

Exploiting the already present bias p<sup>+</sup> strips in SDD (Silicon Drift detectors)

Voltage can be modulated like in a CCD on those strips.



FV CCDs

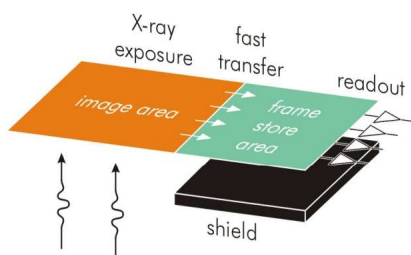
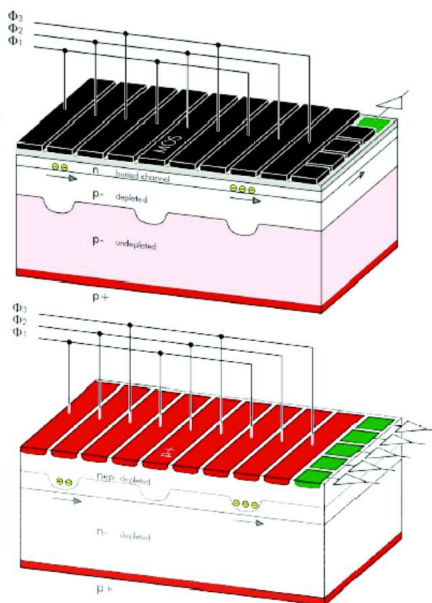
# pn-CCD vs. MOS-CCD

classic CCD structure

MOS CCD

- MOS transfer gates
  - pn junctions
- buried channel
  - deep transfer
- partial depletion
  - full depletion
- front-side illumination
  - backside entrance window
- serial readout
  - 1 preamp per channel

pn CCD

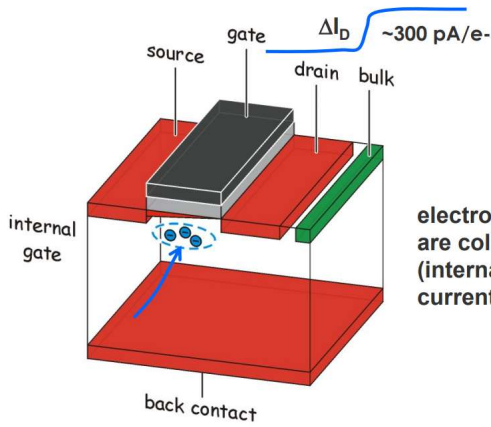


CCD readout can also have smarter configuration:

to allow integration time simultaneously to readout time, we can store the just integrated frame in another area for a readout

PN CCD vs MOS CCD

DEPFET



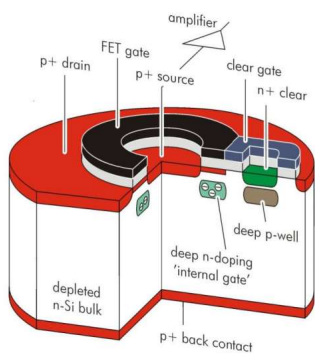
electrons generated in the fully depleted bulk are collected underneath the transistor channel (internal gate) and modulate the source-drain current (~300 pA/e-).

collecting anode = internal gate → no interconnection strays!  
 non-destructive readout → readout on demand!

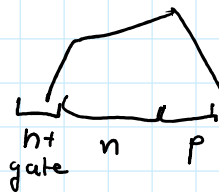
Instead of using MOSFETS for readout, we can use DEP-MOSFET.

The principle is similar to MOSFETS. Instead of having only the external gate, DEP MOS also have an internal gate residing underneath the channel → n+ internal gate

this internal gate is composed of an n+ region



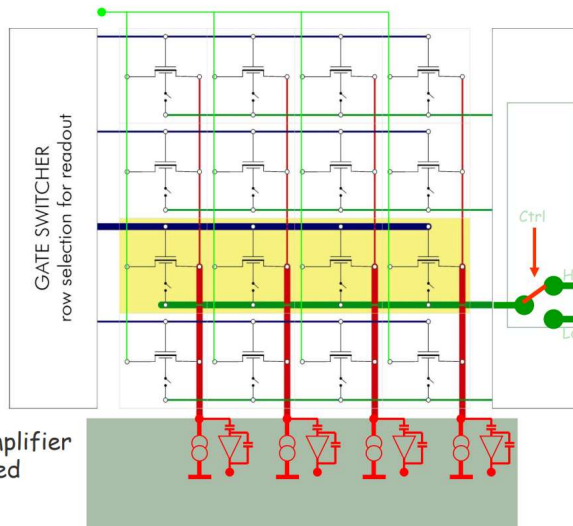
charge is pushed on n+ internal gate (like in a pin junction)



⇒ this charge opens or closes the MOSFET channel using FIELD EFFECT

# DEPFET matrix prototypes

- Global drain contact
- Sources connected column-wise
- Gate, Clear & Cleargate connected row-wise
- Source follower readout: Column biased by current source

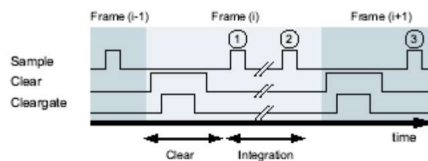
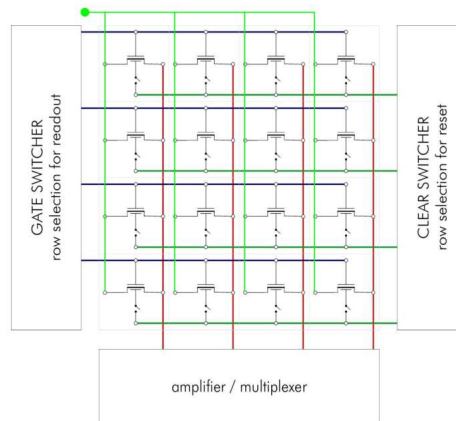


**CAMEX 64 G:**  
64 channel low noise voltage amplifier  
8-fold CDS-filter and integrated sequencer

**Switcher II:**  
Control chip with 64 channels a 2 ports & integrated sequencer  
AMS high voltage CMOS process (up to 20 V)

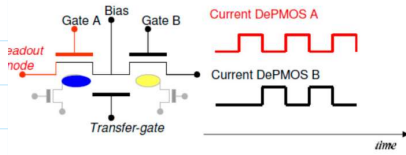
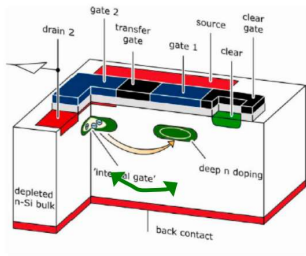
# DEPFETs as pixel detectors

- matrix arrangement allows to turn on transistors individually
- readout of charge in place of origin
  - no "charge transfer loss"
  - no "out-of-time" events
- continuous row-by-row readout (through serial or parallel readout)
- followed by clear of row
- no waiting (charge collection) period needed



DEPFET detectors we used in configuration with 1 DEPFET per pixel, as charge can be collected in the internal gate.

# Multiple readout (ping-pong)



- Noise reduction technique based by repetitive readout of signal charge  
→ ENC ultimate limitation can be broken
- Signal charge measured by current difference between filled and empty internal gate
- Moving signal charge in and out internal gate N times
  - noise reduction !
- We will analyse it for white and 1/f series noise

The ENC can in principle be lowered below the limit of single-pulse (S/N) analysis

## noise analysis

$$ENC^2 = A_1 a C_{tot}^2 \frac{1}{T} + A_2 (\pi A_f) C_{tot}^2 + A_3 (q I_L) \tau$$

p.s. a, b are BILATERAL noise spectral densities,  $A_1$  is the 1/f noise coefficient of the single-sided noise spectral density ( $A_1/f$ )

Schematic representation of the multiple non-destructive readout with fixed total measurement time  $T_{TOT}$ .

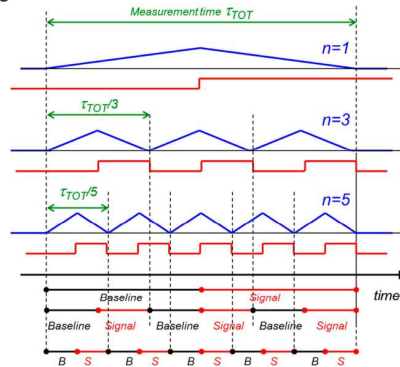
In the upper drawing, only one readout of the signal (red line) is performed, exploiting the whole  $T_{TOT}$ . In the other two cases, the signal is reproduced and read out 3 and 5 times respectively.

The time available for each single measurement is  $T_{TOT}/n$ , number of repetitions.

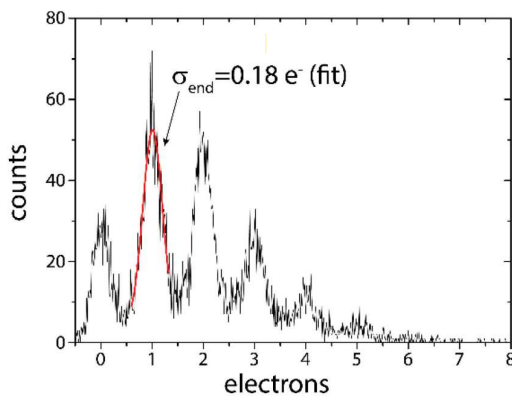
The reduction of the shaping time for each single measurement (look at the blue weighting functions) turns out in an increased r.m.s. value of the white noise of the individual measurements. The averaging effect of the n readouts (3 and 5 respectively) compensates this noise increment. Therefore, the ENC component related to the white voltage noise does not change with the number of measurements in a fixed time interval.

For the 1/f noise the situation is different. The r.m.s. value of noise of one measurement is independent from the measurement time. This means a single measurement of time length  $T_{TOT}/n$  would result in the same r.m.s. noise.

In this case, the averaging effect of n measurements makes the ENC go down with approximately  $\sqrt{n}$ .



White  
 $A_1 \dots \frac{1}{T} = ENC_w^2 \cdot N$   
 Averaging  
 $ENC_w^2 \frac{1}{N} \Rightarrow ENC_w^2 \text{ const}$   
 1/f  
 $ENC_{1/f}^2 \Rightarrow ENC_{1/f} \propto \frac{1}{\sqrt{N}}$



- sub-electron noise achieved !
- increase of ENC for higher no. of readouts most likely related to parallel noise
- single photon imaging in the visible !
- absolute calibration of charge !

Single photon spectrum measured at low light intensity with a circular RNDR-DEPFET at a temperature of  $-55^\circ\text{C}$ . Due to the higher amplification the read noise of a single readout is only  $3.1 e^-$  rms and a minimum noise of  $0.18 e^-$  was obtained with only 300 readouts.